

DATA SHEET

TFT MODULE

**DEM 800480W TMH-PW-N
(A-TOUCH)**

5,0“ TFT

Product Specification

Ver.: 1

Revision History

Revision	Date	Originator	Detail	Remarks
0	28.11.2018	MHI	Initial Release	-
1	10.01.2019	MH	Modify Chromacity Transmissive	P8

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs, a touch panel and a Backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size (Diagonal)	5.0"	-
LCD Type	TN TFT	-
Display Mode	Normally White / Transmissive	-
Resolution	800 x RGB x 480	Pixels
View Direction	12 O'clock	Best Image
Gray Scale Inversion Direction	6 O'clock	-
Module Outline	120.70 x 75.80 x 4.00 (Note1)	mm
Active Area	108.00 x 64.80	mm
Pixel Size	0.1350 x 0.1350	mm
Pixel Arrangement	R.G.B Vertical Stripe	-
Polarizer Surface Treatment	Anti-Glare	-
Display Colors	16.7 Million	-
Interface	24-Bit RGB interface	-
With or Without Touch Panel	With	-
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	~72	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

V_{SS}=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	DVDD	-0.3	5.0	V
	AVDD	-0.5	13.5	V
	VGH	-0.3	42	V
	VGL	-20	0.3	V
Storage Temperature	T _{STG}	-30	+80	°C
Operating Temperature	T _{OP}	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C and the back ground will become darker at high temperature operating.

4. DC Characteristics

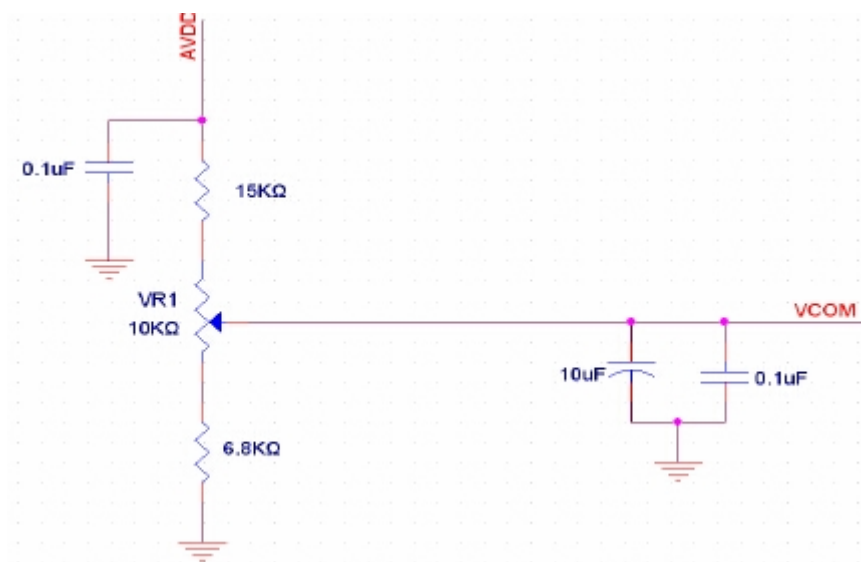
Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	3.0	3.3	3.6	V
	AVDD	10.2	10.4	10.6	V
	VGH	15.3	16.0	16.7	V
	VGL	-6.7	-6.0	-5.3	V
Input Signal Voltage	VCOM	3.09	4.09	5.09	V
Logic Low Input Voltage	V _{IL}	0	-	0.3*DVDD	V
Logic High Input Voltage	V _{IH}	0.7*DVDD	-	DVDD	V

Note 1: Be sure to apply DVDD and VGL to the LCD first, and then apply VGH.

Note 2: DVDD setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D,L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

Note 4: Typical VCOM is only a reference value. It must be optimized according to each LCM. Please use VR and base on below application circuit.



5. Backlight Characteristics

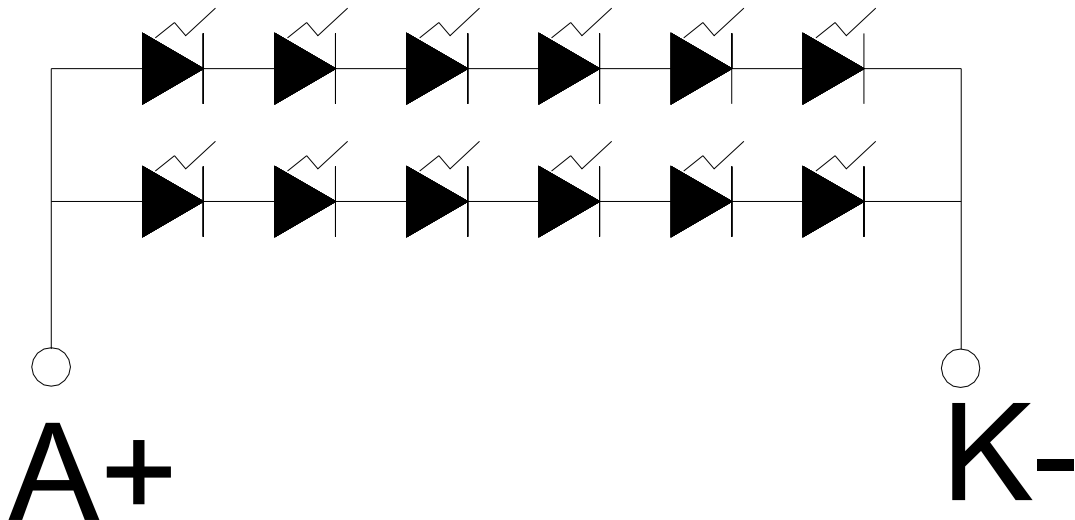
5.1. Backlight Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Backlight Voltage	V _{LED}	T _a =25 °C, I _F =20mA/LED	17	19.2	22	V
Backlight Current	I _{LED}	T _a =25 °C, V _F =3.2V/LED	-	40	-	mA
Power Dissipation	P _D		-	868	-	mW
Uniformity	Avg		70	75	-	%
LED Lifetime (25°C)	-		20000	30000	-	Hrs
Drive Method	Constant Current					
LED Configuration	12 White LEDs(6 LEDs in one String and 2 Groups in Parallel)					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness.

The environmental conducted under ambient air flow, at T_a=25°C±2 °C, 60%RH±5%,
I_F=20mA/LED.

5.2. Backlighting Circuit



6. Touch Screen Panel Specifications**6.1 Electrical Characteristics**

Item	Min.	Typ.	Max.	Unit	Note
Linearity	-1.5	-	1.5	%	Analog X and Y Directions
Terminal Resistance	150	-	450	Ω	Y(Glass Side)
	400	-	900	Ω	X (Film Side)
Insulation Resistance	20	-	-	M Ω	DC $\leq$ 10V
Voltage	-	-	5	V	DC
Chattering	-	-	10	ms	

Caution (1): Do not operate it with a thing except a polyacetal pen (tip R0.8mm or less) or a finger nail, especially those with hard or sharp tips such as a ball point pen or a mechanical pencil.

Caution (2): RTP operation must be followed the parameter condition.

Caution (3): If ask for use glare ITO film , it's will has newton issue.

6.2 Mechanical & Reliability Characteristics

Item	Min.	Typ.	Max.	Unit	Note
Activation Force	30	-	100	g	(1)
Durability-Surface Scratching	Write 20,000	-	-	Characters	(2)
Durability-Surface Pitting	1,000,000	-	-	Touches	(3)
Surface Hardness	3	-	-	H	

Note (1) Stylus pen Input: R0.8mm polyacetal pen or Finger nail

Note (2) Measurement for Surface area

- Force: 150-250gf
- Speed: 60mm/sec
- Stylus: R0.8 polyacetal pen or Finger nail

Note (3) Pit 1,000,000 times on the Film with a R3.75 silicon rubber.

- Force: Force: 2.45N
- Speed: 3times/sec

7. Optical Characteristics

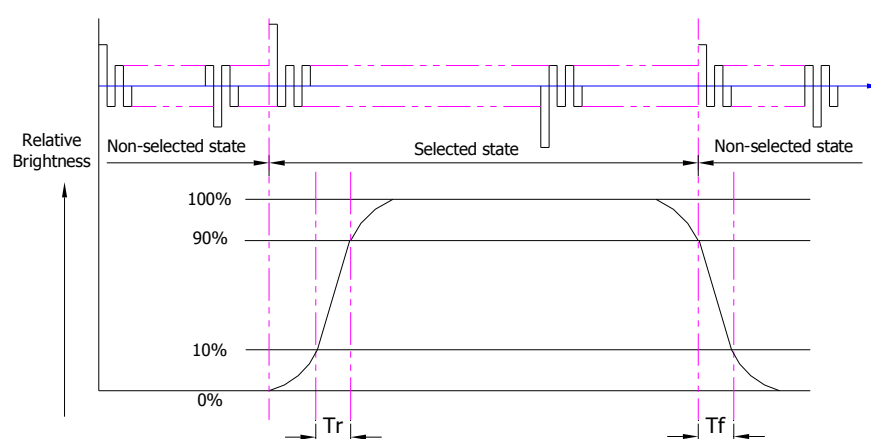
7.1. Optical Characteristics

Ta=25°C, DVDD=3.3V

	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
Backlight On (Transmissive Mode)	Luminance on TFT($I_f=20\text{mA/LED}$)		Lv	Normally Viewing Angle $\theta_x = \phi_y = 0^\circ$	160	200	-	cd/m ²
	Contrast Ratio(See 7.3)		CR		250	350	-	
	Response Time (See 7.2)		Tr+Tf		-	25	50	ms
	Chromaticity Transmissive (See 7.5)	Red	XR		0.519	0.569	0.619	
			YR		0.288	0.338	0.388	
		Green	XG		0.284	0.334	0.384	
			YG		0.560	0.610	0.660	
		Blue	XB		0.094	0.144	0.194	
			YB		0.051	0.101	0.151	
		White	XW		0.262	0.312	0.362	
			YW		0.306	0.356	0.406	
	Viewing Angle (See 7.4)	Horizontal	θ_{x+}	Center CR ≥ 10	60	70	-	Deg.
			θ_{x-}		60	70	-	
		Vertical	ϕ_{y+}		45	50	-	
			ϕ_{y-}		60	70	-	

7.2. Definition of Response Time

7.2.1. Normally Black Type (Negative)

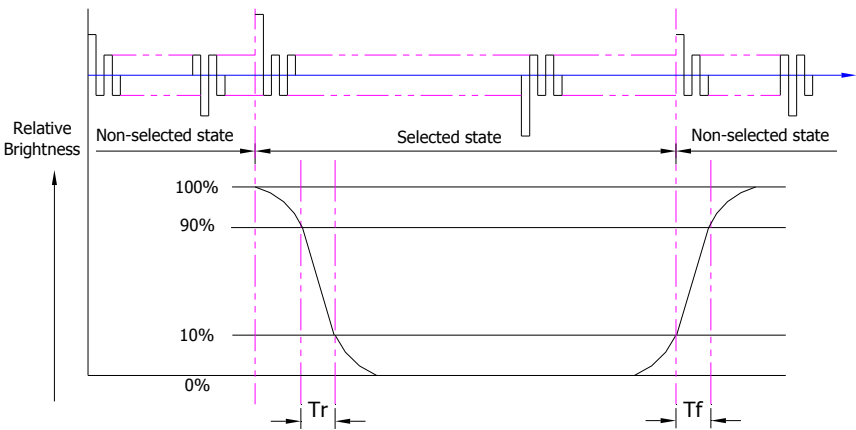


Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%

Note: Measuring machine: LCD-5100

7.2.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected state with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

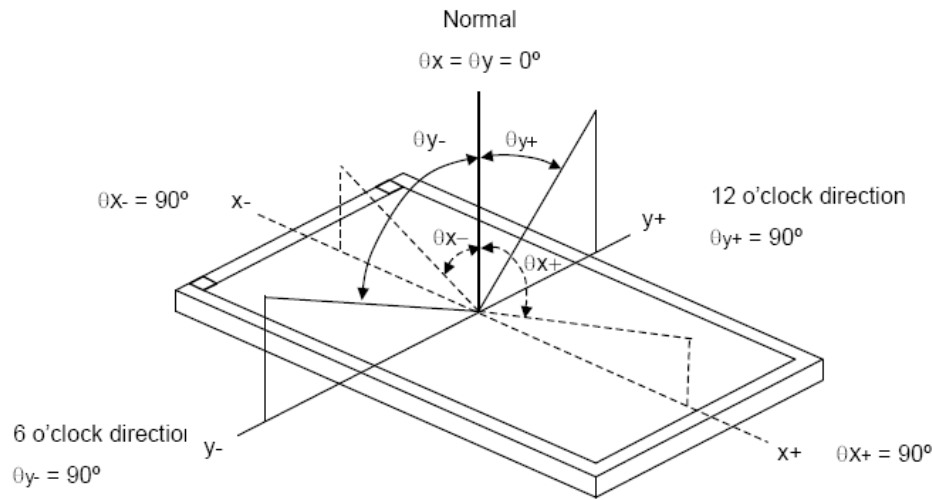
7.3. Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area Centre Point
Test Pattern	A: All Pixels white
	B: All Pixel black
Contrast Setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

7.4. Definition of Viewing Angles



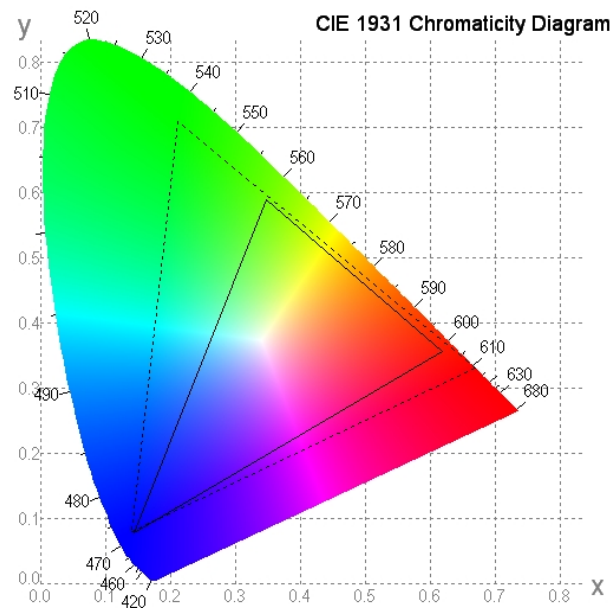
Measuring machine: LCD-5100 or EQUI

7.5. Definition of Color Appearance

R, G, B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)

**7.6. Definition of Surface Luminance, Uniformity and Transmittance**

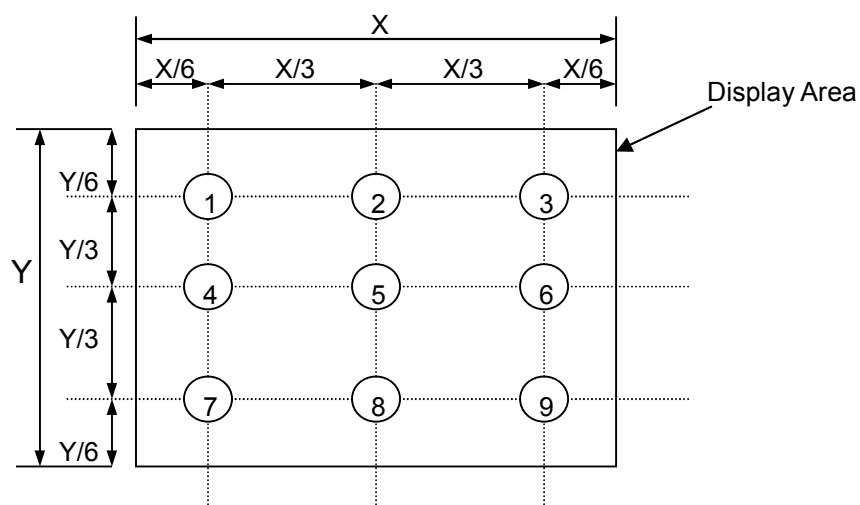
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

7.6.1. Surface Luminance: $L_v = \text{average } (L_{P1}:L_{P9})$

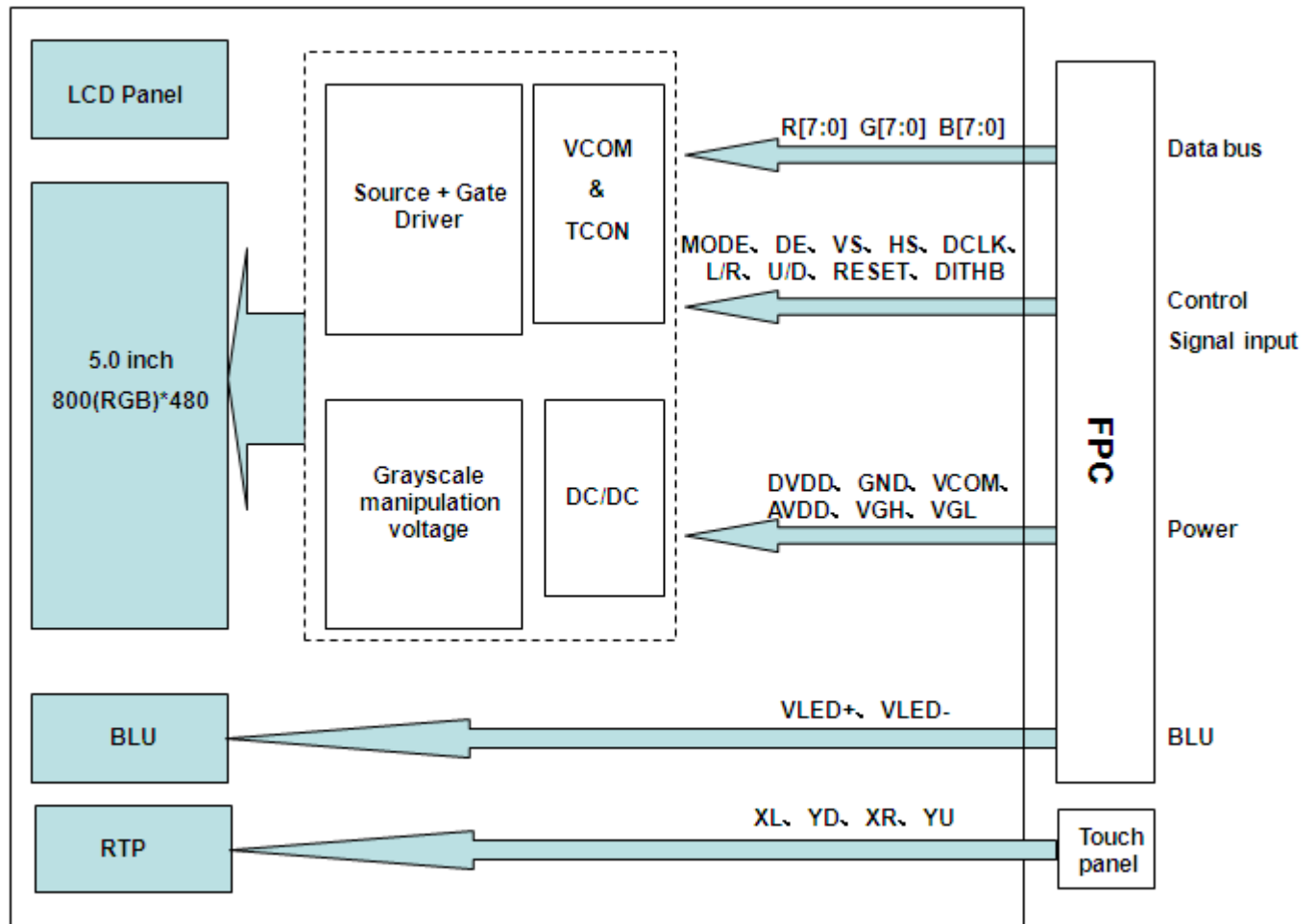
7.6.2. Uniformity = $\text{Minimal } (L_{P1}:L_{P9}) / \text{Maximal } (L_{P1}:L_{P9}) * 100\%$

7.6.3. Transmittance = $L_v \text{ on LCD} / L_v \text{ on Backlight} * 100\%$

Note: Measuring machine: BM-7



8. Block Diagram and Power Supply



9. Interface Pins Definition

No.	Symbol	Function	Remark
1	VLED+	LED Input Terminal I(Anode).	
2	VLED+	LED Input Terminal I(Anode).	
3	VLED-	Ground (Cathode).	
4	VLED-	Ground (Cathode).	
5	GND	Ground.	
6	VCOM	Common voltage.	
7	DVDD	Power for Digital Circuit.	
8	MODE	DE/SYNC mode select.	Note1
9	DE	Data Input Enable.	
10	VS	Vertical Sync Input.	
11	HS	Horizontal Sync Input.	
12	B7	Blue data(MSB)	
13	B6	Blue data	
14	B5	Blue data	
15	B4	Blue data	
16	B3	Blue data	
17	B2	Blue data	
18	B1	Blue data	Note 2
19	B0	Blue data(LSB)	Note 2
20	G7	Green data(MSB)	
21	G6	Green data	
22	G5	Green data	
23	G4	Green data	
24	G3	Green data	
25	G2	Green data	
26	G1	Green data	Note 2
27	G0	Green data(LSB)	Note 2
28	R7	Red data(MSB)	
29	R6	Red data	
30	R5	Red data	
31	R4	Red data	
32	R3	Red data	
33	R2	Red data	
34	R1	Red data	Note 2
35	R0	Red data(LSB)	Note 2
36	GND	Ground.	
37	DCLK	Sample clock.	Note 3
38	GND	Ground.	
39	L/R	Left / right selection.	Note 4,5
40	U/D	Up / Down selection.	Note 4,5
41	VGH	Gate ON Voltage.	

42	VGL	Gate OFF Voltage.	
43	AVDD	Power for Analog Circuit.	
44	RESET	Global reset pin.	Note 6
45	NC	No connection.	
46	VCOM	Common voltage.	Note 7
47	DITHB	Dithering function.	
48	GND	Ground.	
49	NC	No connection.	
50	NC	No connection.	

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE="0", DE must be grounded.

Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

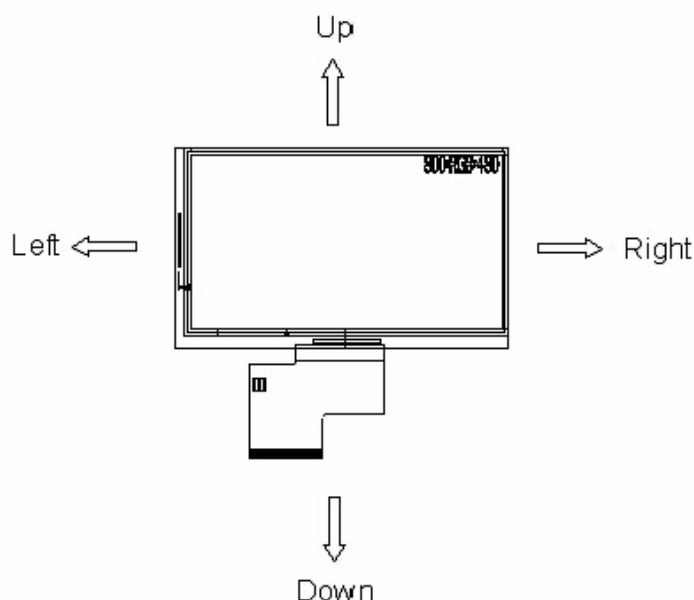
Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode.

Setting of Scan Control Input		Scanning Direction
U/D	L/R	
GND	DVDD	Up to down, left to right
DVDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
DVDD	DVDD	Down to up, left to right

Note 5: Definition of scanning direction.

Refer to the figure as follow:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with and RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high.

When DITHB="1", Disable internal dithering function.

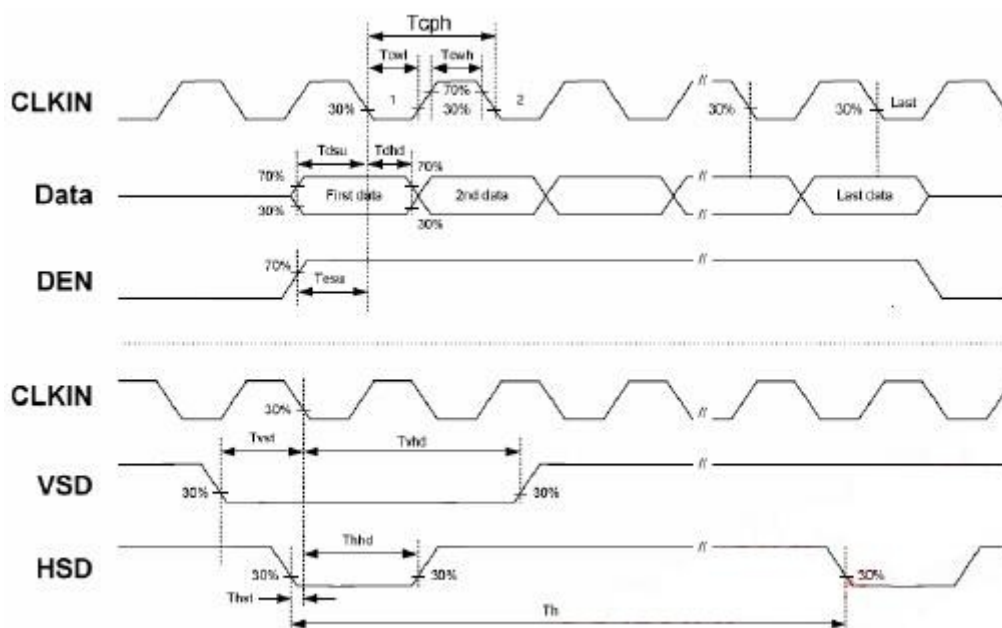
When DITHB="0", Enable internal dithering function.

RTP:

No.	Symbol	Function	Remark
1	XL	Touch panel terminal.	
2	YD	Touch panel terminal.	
3	XR	Touch panel terminal.	
4	YU	Touch panel terminal.	

10. AC characteristic**10.1. Timing**

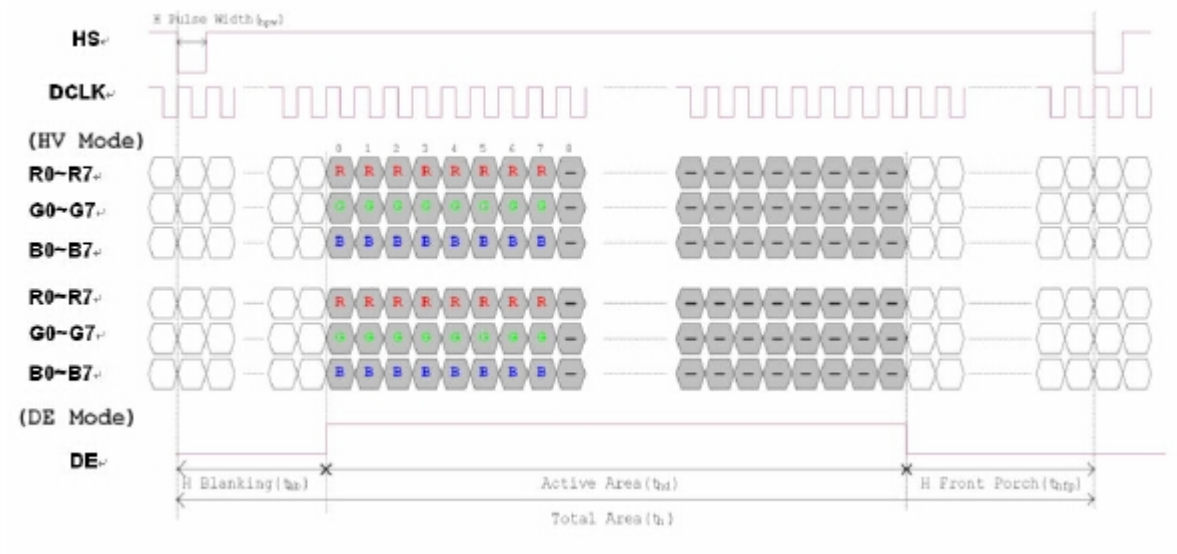
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
HS setup time	T _{hst}	8	-	-	ns	
HS hold time	T _{hhd}	8	-	-	ns	
VS setup time	T _{vst}	8	-	-	ns	
VS hold time	T _{vhd}	8	-	-	ns	
Data setup time	T _{dsu}	8	-	-	ns	
Data hold time	T _{dhd}	8	-	-	ns	
DE setup time	T _{esu}	8	-	-	ns	
DE hold time	T _{ehd}	8	-	-	ns	
DV _{DD} Power On Slew rate	T _{POR}	-	-	20	ms	From 0 to 90% DV _{DD}
RESET pulse width	T _{Rst}	1	-	-	ms	
DCLK cycle time	T _{coh}	20	-	-	ns	
DCLK pulse duty	T _{cwh}	40	50	60	%	



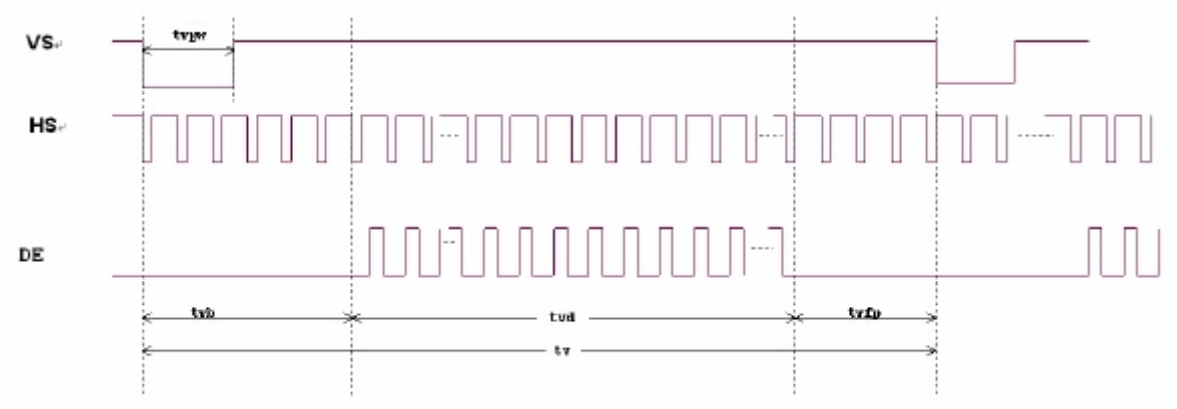
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	-	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

Data Input Format :



Horizontal Input Timing Diagram

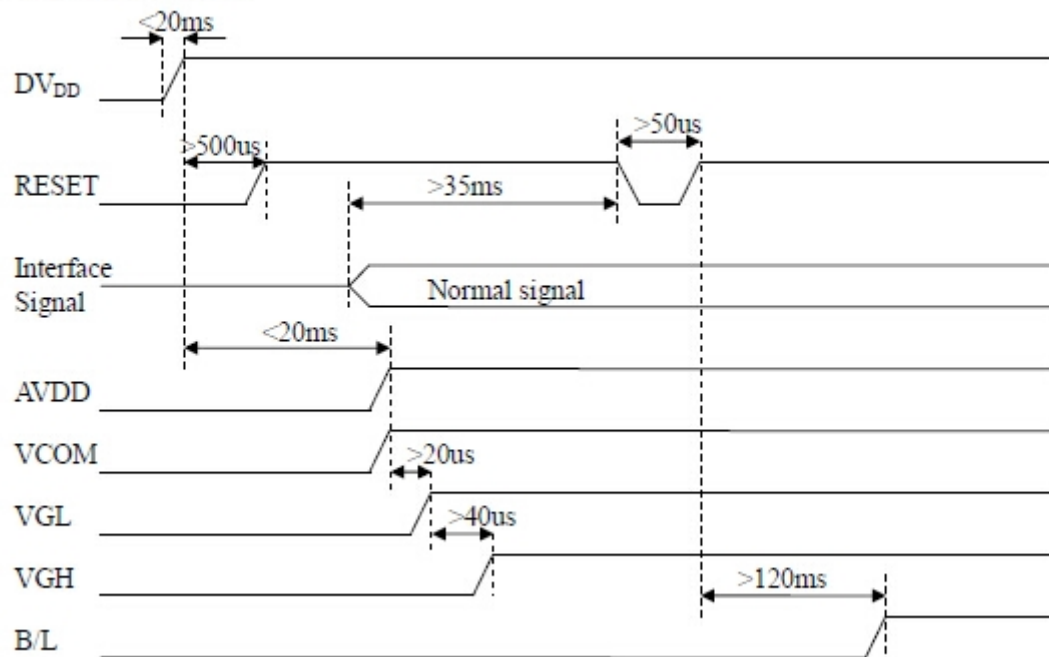


Vertical Input Timing Diagram

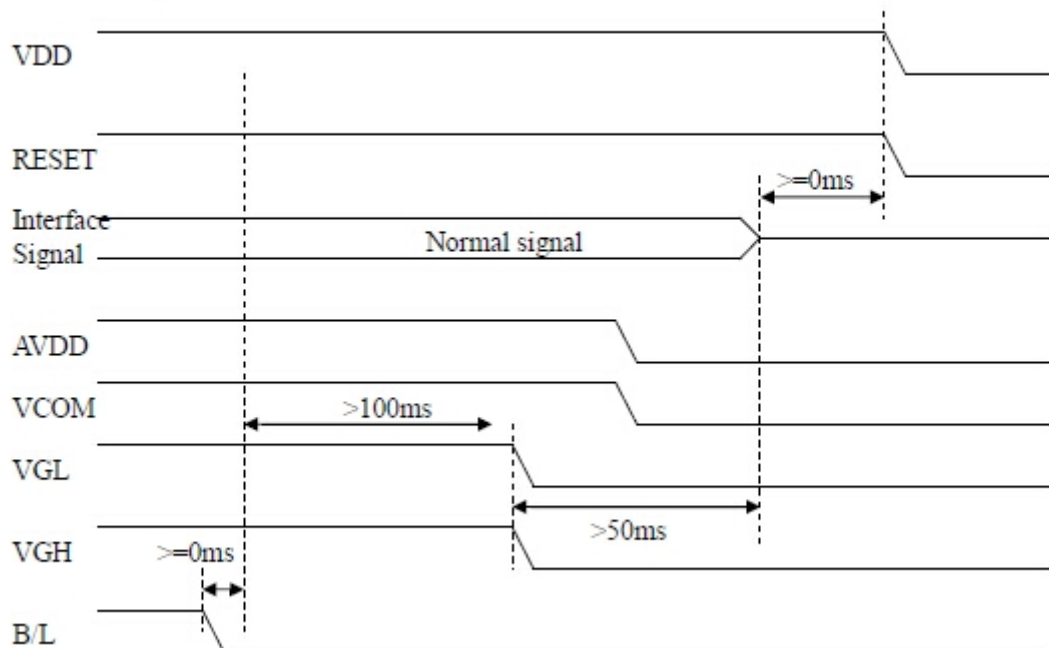
10.2. Power ON/Off Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

a. Power on sequence:



b. Power off sequence:



11. Quality Assurance

11.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

11.2.Standard for Quality Test

11.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

11.2.2. Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

11.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

11.3.Nonconforming Analysis & Disposition

11.3.1. Nonconforming analysis:

11.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

11.3.2. Disposition of nonconforming:

11.3.2.1. Non-conforming product over PPM level will be replaced.

11.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

11.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1. There is any discrepancy in standard of quality assurance.

11.4.2. Additional requirement to be added in product specification.

11.4.3. Any other special problem.

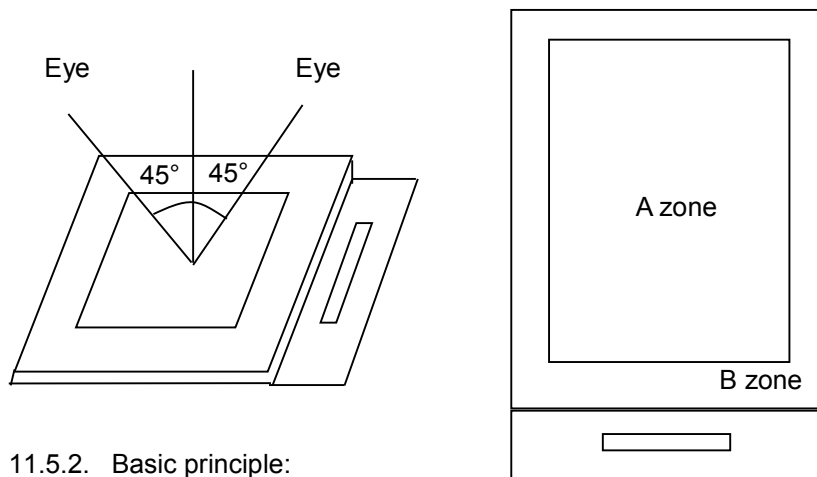
11.5. Standard of the Product Visual Inspection

11.5.1. Appearance inspection:

11.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,

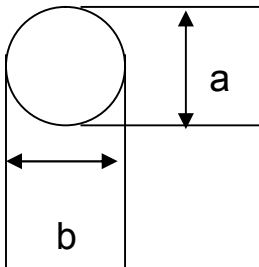


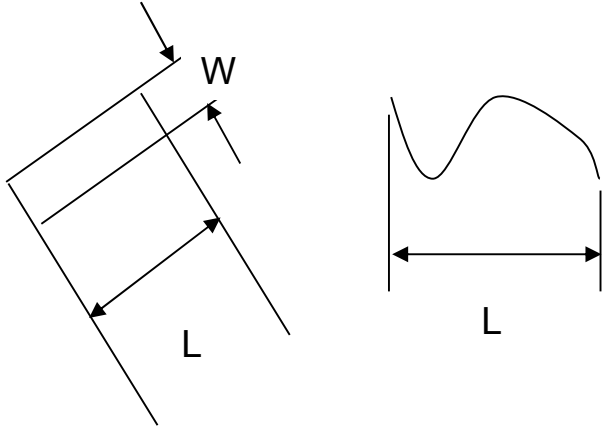
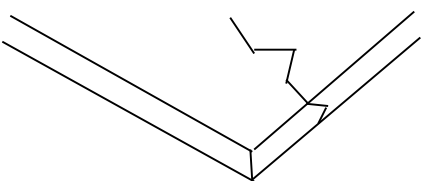
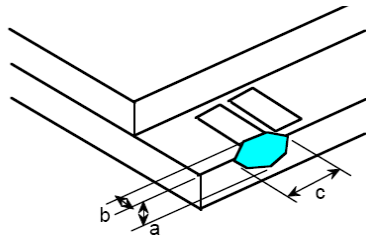
11.5.2. Basic principle:

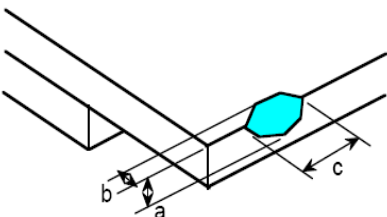
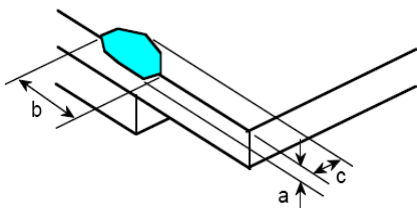
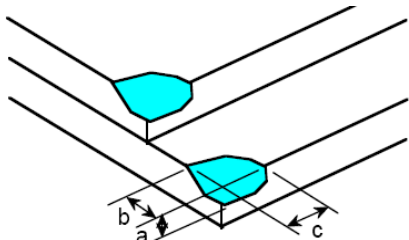
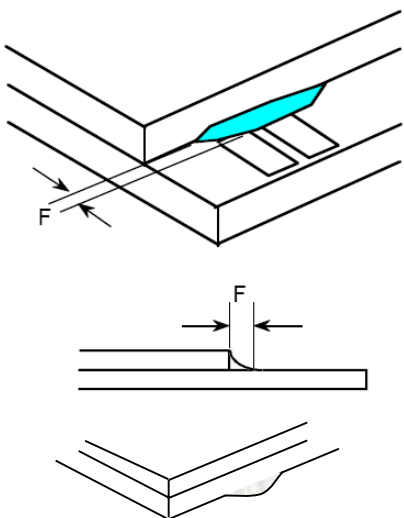
11.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

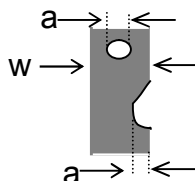
11.5.2.2. New item must be added on time when it is necessary.

11.6. Inspection Specification

No.	Item	Criteria (Unit: mm)																		
01	Black / White Spot Foreign Material (Round Type) Pinholes Stain Particles Inside Cell. (Minor Defect)	 $\varphi = (a + b) / 2$	<table><tr><th>Size \ Area</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.20$</td><td>Ignore</td></tr><tr><td>$0.20 < \varphi \leq 0.50$</td><td>$N \leq 3$</td></tr><tr><td>$0.50 < \varphi$</td><td>0</td></tr></table>		Size \ Area	Acc. Qty	$\varphi \leq 0.20$	Ignore	$0.20 < \varphi \leq 0.50$	$N \leq 3$	$0.50 < \varphi$	0								
		Size \ Area	Acc. Qty																	
$\varphi \leq 0.20$	Ignore																			
$0.20 < \varphi \leq 0.50$	$N \leq 3$																			
$0.50 < \varphi$	0																			
		Distance between 2 defects should more than 5mm apart.																		
02	Electrical Defect (Minor Defect)	<table><tr><th rowspan="2">Bright dot</th><th>Display Area</th><th>Total</th><th rowspan="4">Note1</th></tr><tr><td>$N \leq 2$</td><td>$N \leq 2$</td></tr><tr><th>Dark dot</th><td>$N \leq 4$</td><td>$N \leq 4$</td></tr><tr><th>Total dot</th><td>$N \leq 4$</td><td>$N \leq 4$</td></tr><tr><th>Mura</th><td colspan="2">Not visible through 5% ND filters.</td><th>Note 2</th></tr></table>			Bright dot	Display Area	Total	Note1	$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 4$	$N \leq 4$	Total dot	$N \leq 4$	$N \leq 4$	Mura	Not visible through 5% ND filters.		Note 2
		Bright dot	Display Area	Total		Note1														
$N \leq 2$	$N \leq 2$																			
Dark dot	$N \leq 4$	$N \leq 4$																		
Total dot	$N \leq 4$	$N \leq 4$																		
Mura	Not visible through 5% ND filters.		Note 2																	
		Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.																		

03	Black and White Line Scratch Foreign Material (Line Type) (Minor Defect)	 <table border="1" data-bbox="569 636 1195 904"><thead><tr><th>Length</th><th>Width</th><th>Acc. Qty</th></tr></thead><tbody><tr><td>/</td><td>$W \leq 0.1$</td><td>Ignore</td></tr><tr><td>$L \leq 2.5$</td><td>$0.1 < W \leq 0.2$</td><td>3</td></tr><tr><td>$L > 2.5$</td><td>$0.2 < W$</td><td>0</td></tr><tr><td colspan="2">Total</td><td>3</td></tr></tbody></table> Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
04	Glass Crack (Minor Defect)	 Crack is potential to enlarge, any type is not allowed.															
05	Glass Chipping Pad Area: (Minor Defect)	 <table border="1" data-bbox="873 1487 1342 1655"><thead><tr><th>Length and Width</th><th>Acc. Qty</th></tr></thead><tbody><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>3</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></tbody></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$								
Length and Width	Acc. Qty																
$c > 3.0, b < 1.0$	1																
$c < 3.0, b < 1.0$	3																
$a < \text{Glass Thickness}$																	

06	Glass Chipping Rear of Pad Area: (Minor Defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>2</td></tr><tr><td>$c < 3.0, b < 0.5$</td><td>4</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
07	Glass Chipping Except Pad Area: (Minor Defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>2</td></tr><tr><td>$c < 3.0, b < 0.5$</td><td>4</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
08	Glass Corner Chipping: (Minor Defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c < 3.0, b < 3.0$</td><td>Ignore</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
09	Glass Burr: (Minor Defect) 	<table><tr><th>Length</th><th>Acc. Qty</th></tr><tr><td>$F < 1.0$</td><td>Ignore</td></tr></table> Glass burr don't affect assemble and module dimension.	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	FPC Defect: (Minor Defect) 		10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.									
11	Bubble on Polarizer (Minor Defect)		<table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.30$</td><td>Ignore</td></tr><tr><td>$0.30 < \varphi \leq 0.50$</td><td>$N \leq 2$</td></tr><tr><td>$0.50 < \varphi$</td><td>$N = 0$</td></tr></table>	Diameter	Acc. Qty	$\varphi \leq 0.30$	Ignore	$0.30 < \varphi \leq 0.50$	$N \leq 2$	$0.50 < \varphi$	$N = 0$	
Diameter	Acc. Qty											
$\varphi \leq 0.30$	Ignore											
$0.30 < \varphi \leq 0.50$	$N \leq 2$											
$0.50 < \varphi$	$N = 0$											
12	Dent on Polarizer (Minor Defect)		<table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.25$</td><td>Ignore</td></tr><tr><td>$0.25 < \varphi \leq 0.50$</td><td>$N \leq 4$</td></tr><tr><td>$0.50 < \varphi$</td><td>None</td></tr></table>	Diameter	Acc. Qty	$\varphi \leq 0.25$	Ignore	$0.25 < \varphi \leq 0.50$	$N \leq 4$	$0.50 < \varphi$	None	
Diameter	Acc. Qty											
$\varphi \leq 0.25$	Ignore											
$0.25 < \varphi \leq 0.50$	$N \leq 4$											
$0.50 < \varphi$	None											
13	Bezel	13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.										
14	Touch Panel	D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.										
15	PCB	15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F.										
16	LCD Ripple	Touch the touch panel, cannot see the LCD ripple. Pen: R 0.8mm silicon rubber. Operation Force:120g										
17	Soldering	Follow IPC-A-610C standard										

18	Electrical Defect (Major Defect)	The below defects must be rejected. 18.1 Missing vertical / horizontal segment, 18.2 Abnormal Display. 18.3 No function or no display. 18.4 Current exceeds product specifications. 18.5 LCD viewing angle defect. 18.6 No Backlight. 18.7 Dark Backlight. 18.8 Touch Panel no function.
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Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

11.7. Classification of Defects

11.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

11.7.2. Two minor defects are equal to one major in lot sampling inspection.

11.8. Identification/markings criteria

Any unit with illegible / wrong / double or no marking/ label shall be rejected.

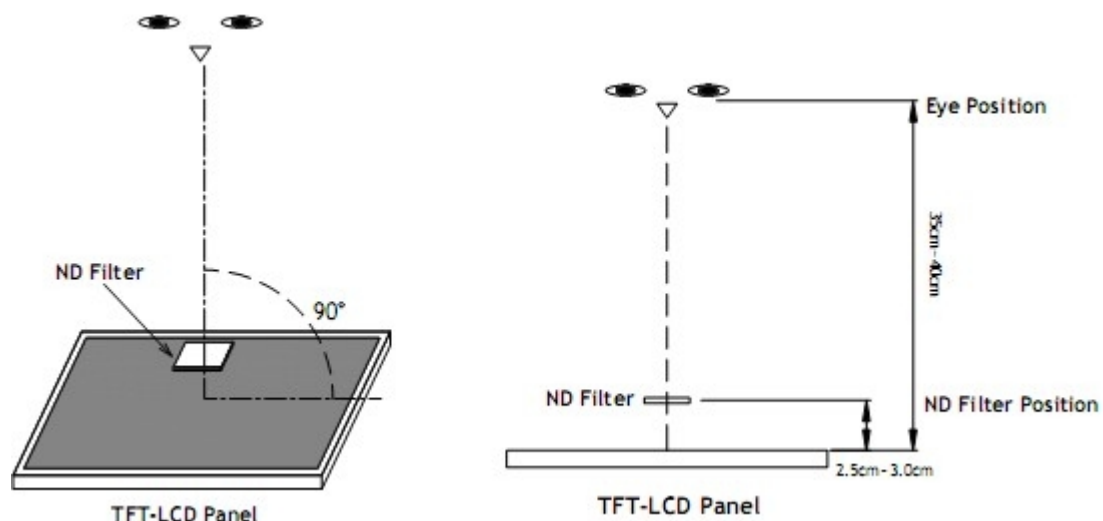
11.9. Packing

11.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

11.9.2. Modules inside package box should have compliant mark.

11.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

12. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity	50°C, 90%RH, 96Hrs	2	GB/T2423.3-2006
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air: $\pm 8\text{kV}$ 150pF/330 Ω 5 times Contact: $\pm 4\text{kV}$ 150pF/330 Ω 5 times	2	GB/T17626.2-2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No defection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

13. Precautions and Warranty

13.1. Safety

- 13.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 13.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

13.2. Handling

- 13.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 13.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

13.3. Storage

- 13.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 13.3.2. Strong light exposure causes degradation of polarizer and color filter.

13.4. Metal Pin (Apply to Products with Metal Pins)

13.4.1. Pins of LCD and Backlight

- 13.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

13.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

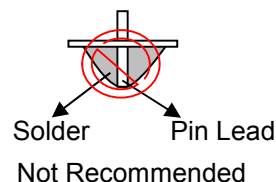
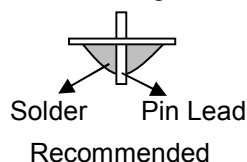
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

13.4.1.3. Solder Wetting



13.4.2. Pins of EL

- 13.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

- 13.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

13.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

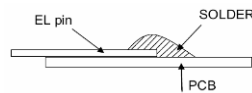
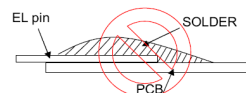
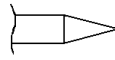
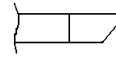
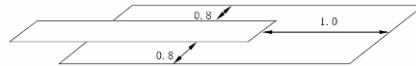
Recommended Solder Temperature: 270~290℃

Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body): 2.0mm

- 13.4.2.4. No horizontal press on the EL leads during soldering.

- 13.4.2.5. 180° bend EL leads three times is not allowed.

13.4.2.6. Solder Wetting**Recommended****Not Recommended****13.4.2.7. The type of the solder iron:****Recommended****Not Recommended****13.4.2.8. Solder Pad****13.5.Operation**

- 13.5.1. Do not drive LCD with DC voltage
- 13.5.2. Response time will increase below lower temperature
- 13.5.3. Display may change color with different temperature
- 13.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.
- 13.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 13.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 13.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 13.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

13.6.Static Electricity

- 13.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 13.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 13.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

13.7.Limited Warranty

- 13.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 13.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 13.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

14. Outline Drawing

