

Display Elektronik GmbH

DATA SHEET

TFT MODULE

DEM 1280092A VM-PW-N

23,2"

Product Specification

Ver.: 0

21.11.2019

Revision History

Revision	Date	Originator	Detail	Remarks
0	21.11.2019	ZDT	Initial Release	

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs, and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size (Diagonal)	23.2"	
Display Mode	Transmissive / Normally Black	
Resolution	1280 RGB x 92	Pixels
View Direction	FULL VIEW	Best Image
Module Outline	611.30 x 62.60 x 10.10 Max (Note1)	mm
Active Area	588.48 x 42.30	mm
Pixel Pitch	0.45975 x 0.45975	mm
Pixel Arrangement	RGB Vertical stripe	
Display Colors	16.7 Million	
Interface	LVDS Interface	
Drive IC	HX8159 & HX8658	
With or Without Touch Panel	Without	
Operating Temperature	0~50	°C
Storage Temperature	-20~60	°C
Weight	TBD	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

GND=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	V _{CC}	-0.5	4.0	V
Storage Temperature	T _{STG}	-20	+60	°C
Operating Temperature	T _{OP}	0	+50	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	Vcc	4.75	5	5.25	V
Current Consumption All white	Icc	-	TBD	-	mA

5. Backlight Characteristic

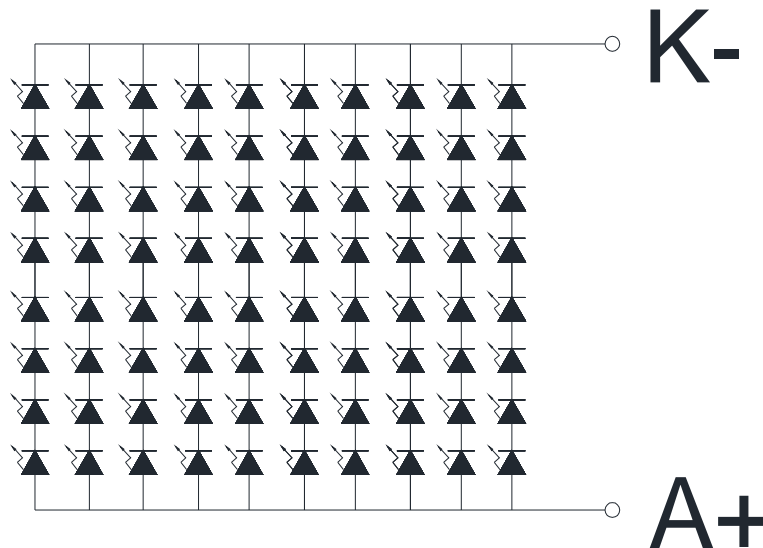
5.1. Backlight Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V _F	T _a =25 °C	21.6	22.8	26.4	V
Forward Current	I _F	T _a =25 °C	-	400	-	mA
Power Dissipation	P _D	-	-	9120	-	mW
Uniformity	Avg	-	-	80	-	%
LED Lifetime (25°C)	-	-	-	15000	-	Hrs
Drive Method	Constant Current					
LED Configuration	80 White LEDs (8 LEDs in one string and 10 groups in parallel)					

Note1 : LED life time defined as follows: The final brightness is at 50% of original brightness.

The environmental conducted under ambient air flow, at T_a=25± 2 °C,60%RH± 5%, I_F=400mA.

5.2. Backlight Characteristic



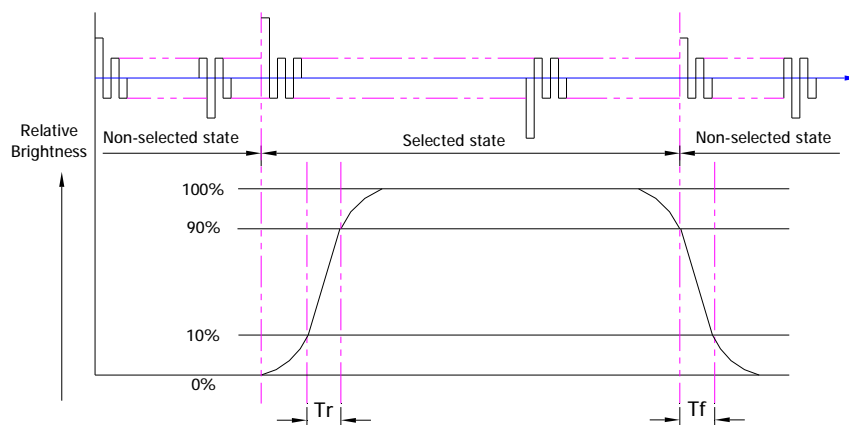
6. Optical Characteristics

Ta=25°C, Vcc=3.3V

	Item	Symbol	Condition	Specification			Unit	
				Min.	Typ.	Max.		
Backlight On (Transmissive Mode)	Luminance On TFT(I _f =400mA)	Lv		880	1100	-	cd/m ²	
	Contrast Ratio(See 6.3)	CR		600	900	-		
	Response Time (See 6.2)	TR+TF		-	15	20	ms	
	Chromaticity Transmissive (See 6.5)	Red	X _R	Center CR≥10	-	TBD	-	
			Y _R		-	TBD	-	
		Green	X _G		-	TBD	-	
			Y _G		-	TBD	-	
		Blue	X _B		-	TBD	-	
			Y _B		-	TBD	-	
	White	X _W	-	TBD	-			
Y _W	-	TBD	-					
Viewing Angle (See 6.4)	Horizontal	θ _{X+}	Center CR≥10	80	-	-	Deg.	
		θ _{X-}		80	-	-		
	Vertical	φ _{Y+}		70	-	-		
		φ _{Y-}		60	-	-		
NTSC ratio (Color gamut)				68	72	-	%	

6.1. Definition of Response Time

6.1.1. Normally Black Type (Negative)

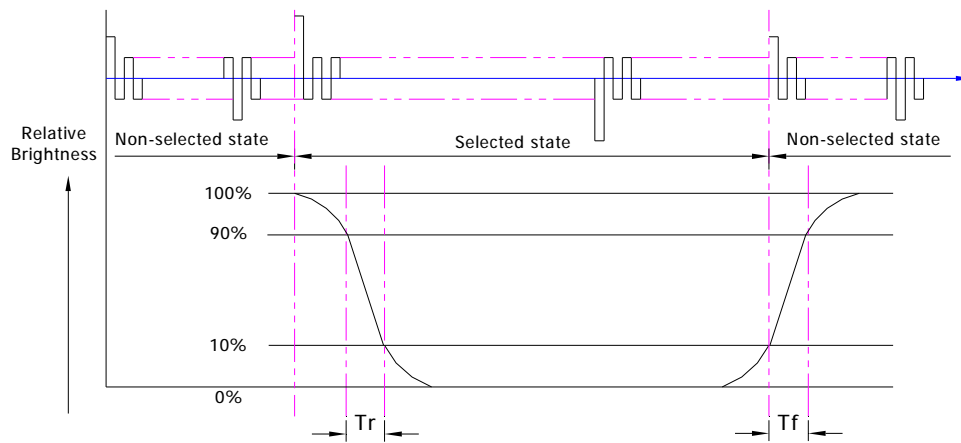


Tr is the time it takes to change form non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note: Measuring machine: LCD-5100

6.1.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

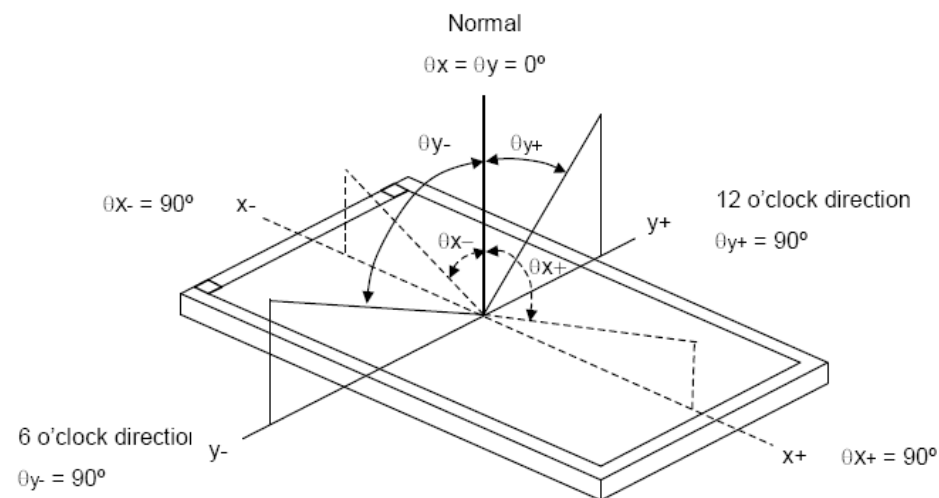
6.2. Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area Centre Point
Test Pattern	A: All Pixels White B: All Pixel Black
Contrast Setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.3. Definition of Viewing Angles



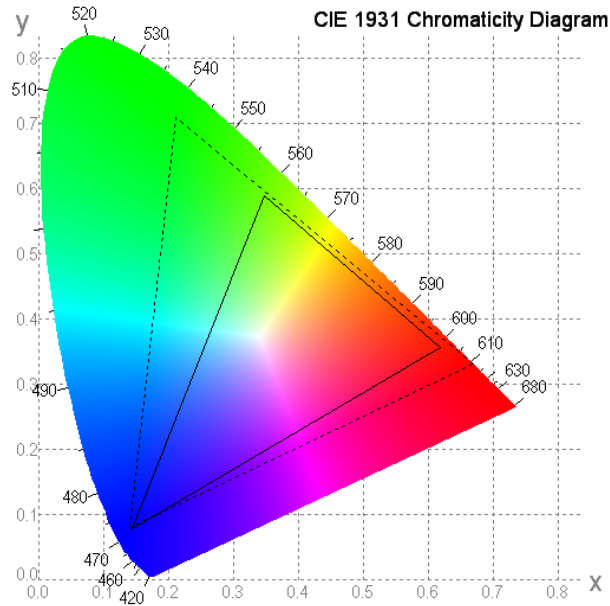
Measuring machine: LCD-5100 or EQUI

6.4. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.5. Definition of Surface Luminance, Uniformity and Transmittance

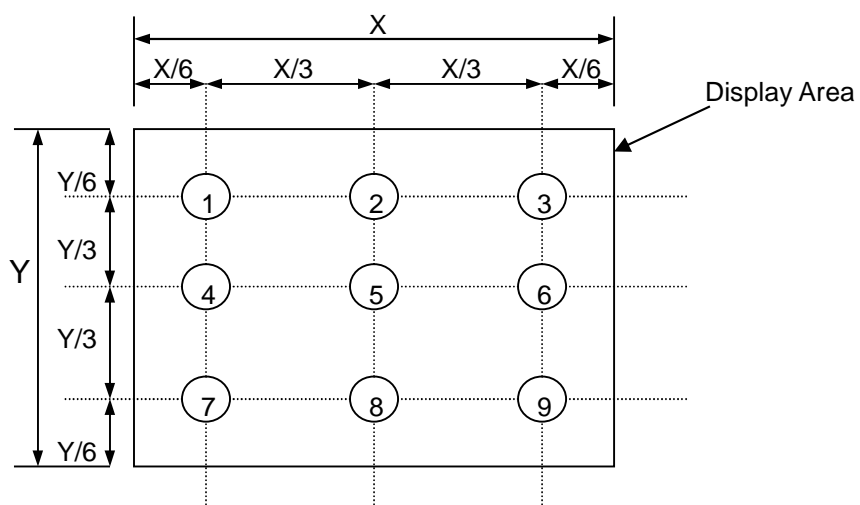
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.5.1. Surface Luminance: $L_v = \text{average} (L_{P1}:L_{P9})$

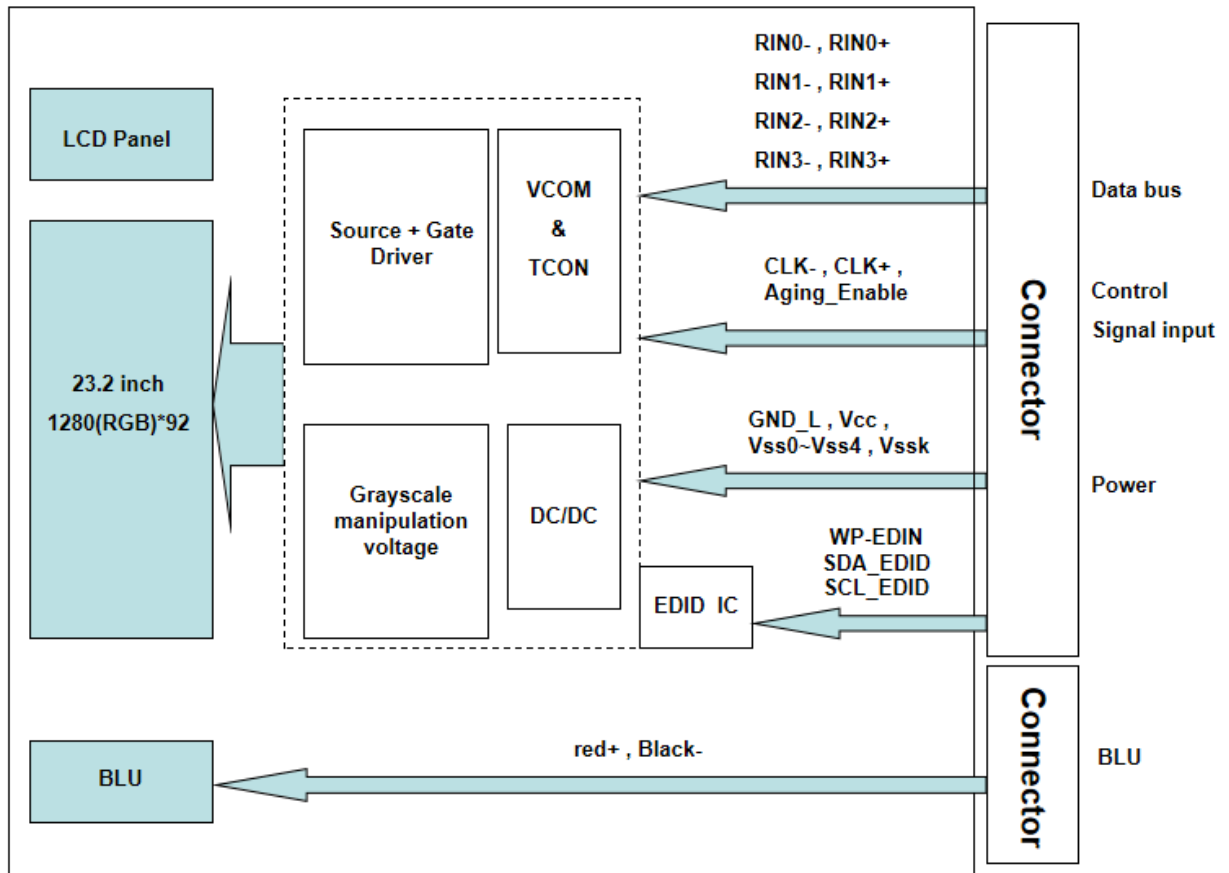
6.5.2. Uniformity = $\text{Minimal} (L_{P1}:L_{P9}) / \text{Maximal} (L_{P1}:L_{P9}) * 100\%$

6.5.3. Transmittance = $L_v \text{ on LCD} / L_v \text{ on Backlight} * 100\%$

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

Connector : MSAK24025P40

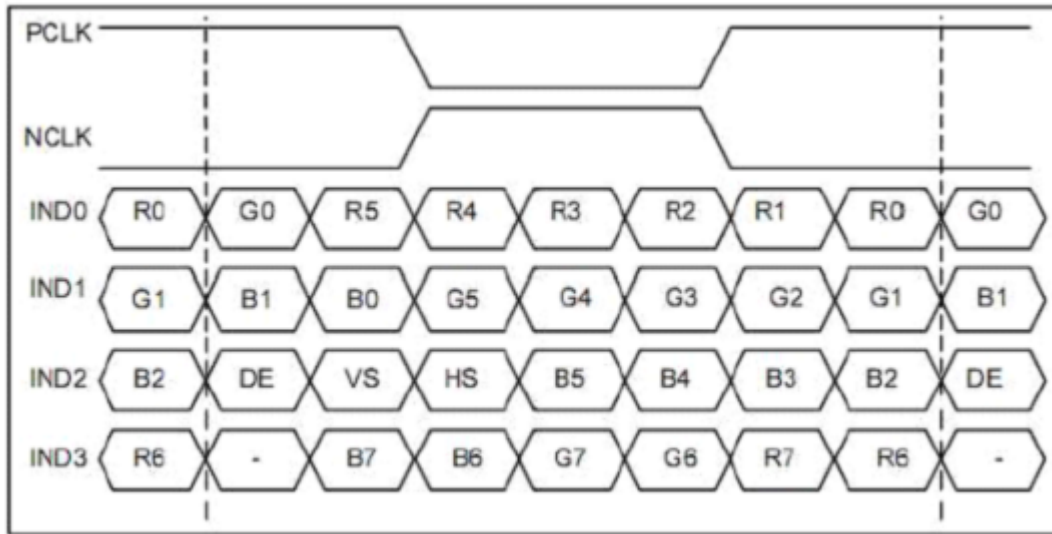
No.	Symbol	Function	Remark
1	Vcc	Logic power : +5V power supply	
2	Vcc	Logic power : +5V power supply	
3	Vcc	Logic power : +5V power supply	
4	GND_L	Logic ground	
5	GND_L	Logic ground	
6	GND_L	Logic ground	
7	Aging_Enable	For aging test , default BIST patterns H:3.3V L:0V	
8	NC	No Connection	
9	NC	No Connection	
10	GND_L	Logic ground	
11	Vss0	Signal 0 ground	
12	RIN0-	-LVDS differential data input channel 0	
13	RIN0+	+LVDS differential data input channel 0	
14	Vss1	Signal 1 ground	
15	RIN1-	-LVDS differential data input channel 1	
16	RIN1+	+LVDS differential data input channel 1	
17	Vss2	Signal 2 ground	
18	RIN2-	-LVDS differential data input channel 2	
19	RIN2+	+LVDS differential data input channel 2	
20	Vssk	Signal K ground	
21	CLK-	-LVDS differential clock input	
22	CLK+	+LVDS differential clock input	
23	Vss3	Signal 3 ground	
24	RIN3-	-LVDS differential data input channel 3	
25	RIN3+	+LVDS differential data input channel 3	
26	Vss4	Signal 4 ground	
27	GND_L	Logic ground	
28	WP_EDID	EDID Write Protection H:3.3V L:0V	
29	SCL_EDID	EDID I ² C Clock Signal H:3.3V L:0V	
30	SDA_EDID	EDID I ² C Data Signal H:3.3V L:0V	
31	GND_L	Logic ground	
32~40	NC	No Connection	

BLU PIN:

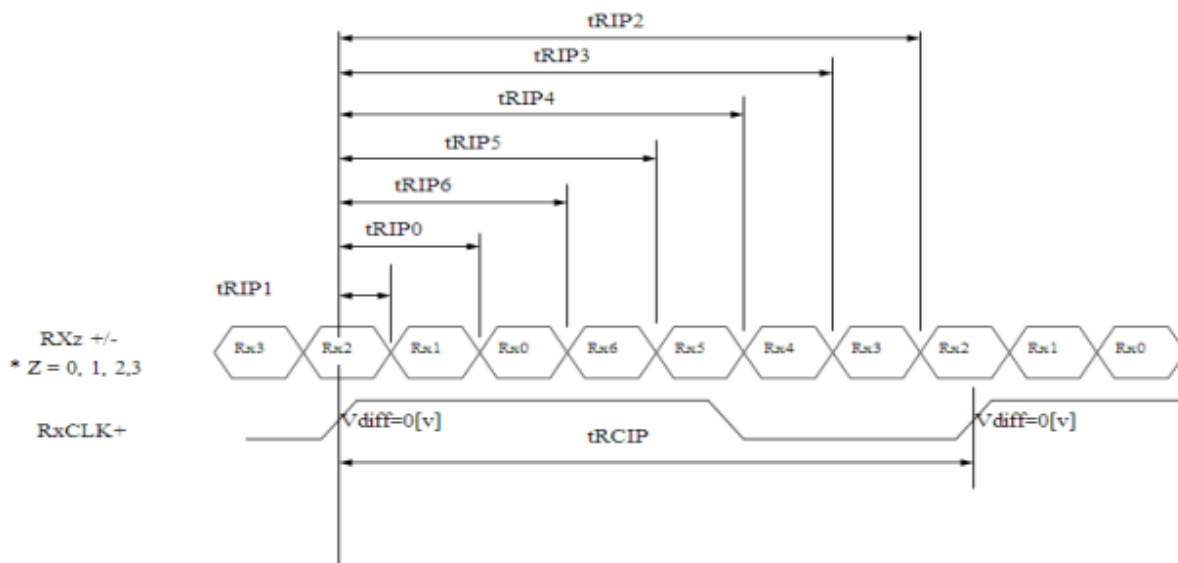
Symbol	Function
Red+	LED Anode
Black-	LED Cathode

9. Timing Characteristics

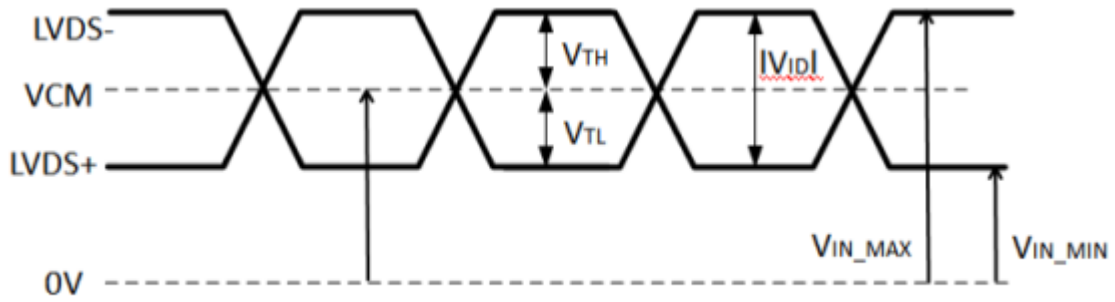
9.1. LVDS Timing



Item	Symbol	Min	Typ	Max	Unit	Remark
CLKIN Period	tRCIP	10.82	13.47	16.54	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 × tRCIP/7-0.4	2 × tRCIP/7	2 × tRCIP/7+0.4	nsec	
Input Data 3	tRIP5	3 × tRCIP/7-0.4	3 × tRCIP/7	3 × tRCIP/7+0.4	nsec	
Input Data 4	tRIP4	4 × tRCIP/7-0.4	4 × tRCIP/7	4 × tRCIP/7+0.4	nsec	
Input Data 5	tRIP3	5 × tRCIP/7-0.4	5 × tRCIP/7	5 × tRCIP/7+0.4	nsec	
Input Data 6	tRIP2	6 × tRCIP/7-0.4	6 × tRCIP/7	6 × tRCIP/7+0.4	nsec	

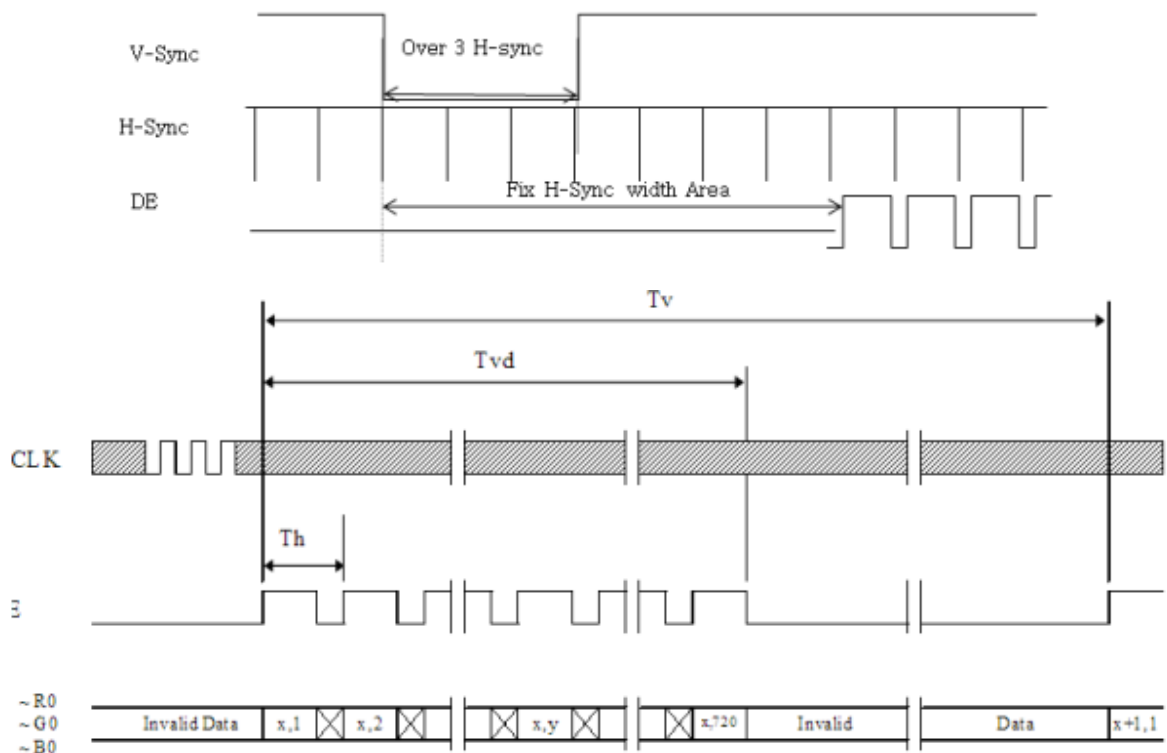


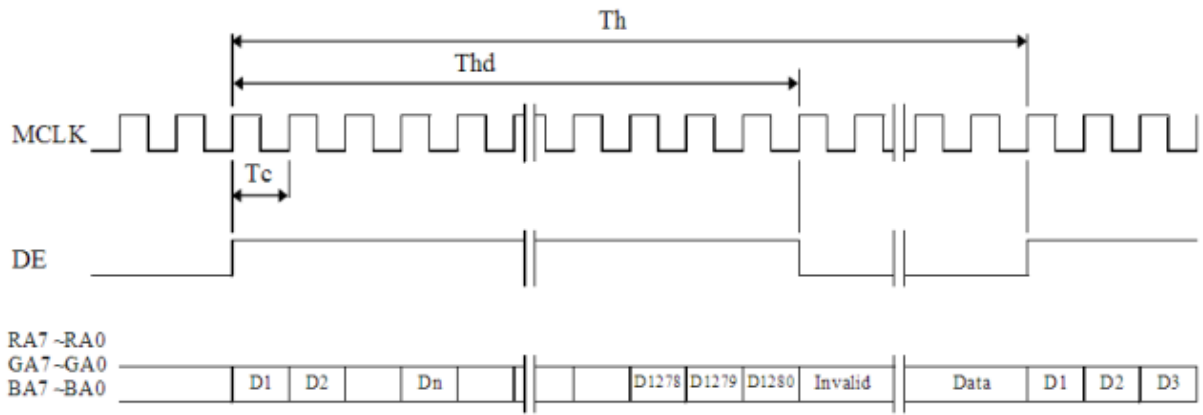
* Vdiff = (RXz+)-(RXz-),..., (RXCLK+)-(RXCLK-)



High Level Differential Input Threshold Voltage	V_{IH}	+100	-	+300	mV
Low Level Differential Input Threshold Voltage	V_{IL}	-300	-	-100	mV
Differential input voltage	$ V_{ID} $	200	-	600	mV
Differential input common mode voltage	V_{cm}	1.0	1.2	1.4	V
Input Voltage Range	V_{in}	0.7	-	1.7	V

9.2. Sync Timing Waveforms

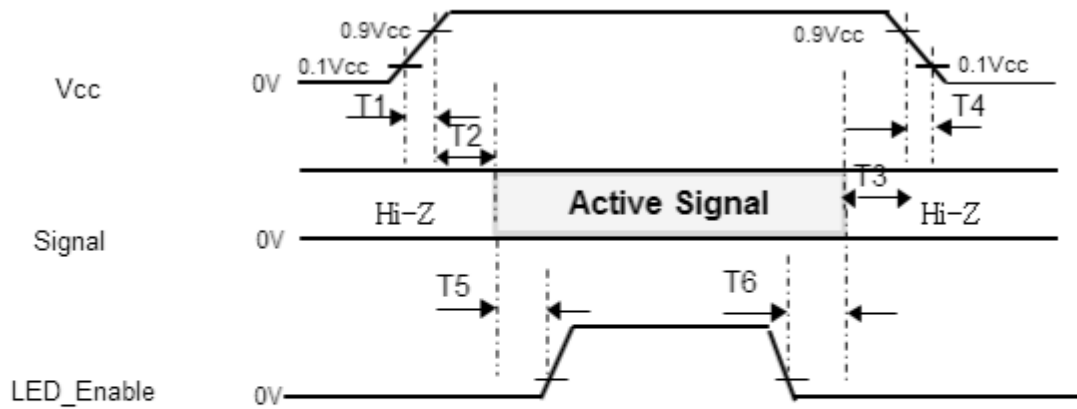




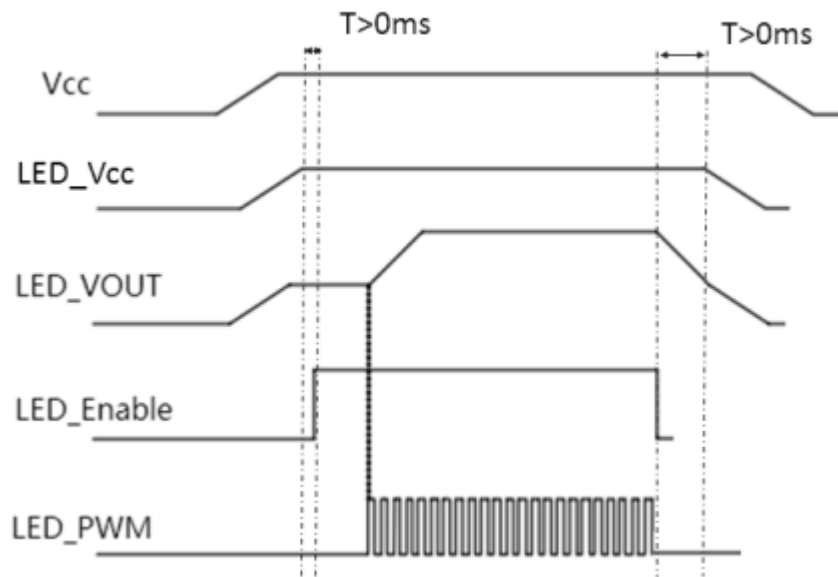
Parameter	Symbol	Min.	Typ.	Max.	Unit
Clock frequency	RxFCLK	60.44	74.25	92.4	MHz
Horizontal Display Area	thd	1280			DCLK
HS Period	th	1380	1650	1750	DCLK
HS Blanking	Thb+thfp	100	370	470	DCLK
Vertical Display Area	tvd	720			TH
VS Period	tv	730	750	880	TH
VS Blanking	Tvbp+tvfp	10	30	160	TH

10. Power Sequence

The power supply ON/OFF setting sequences is illustrated in figure below.



- $0.5\text{ms} \leq T1 \leq 10\text{ms}$
- $0\text{ms} \leq T2$
- $0\text{ms} \leq T3$
- $0\text{ms} \leq T4 \leq 10\text{ms}$
- $100\text{ms} \leq T5$
- $100\text{ms} \leq T6$



Note: There is no tight timing between EN and PWM signal.

11. Quality Assurance

11.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

11.2.Standard for Quality Test

11.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

11.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

11.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

11.3.Nonconforming Analysis & Disposition

11.3.1. Nonconforming analysis:

11.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

11.3.2. Disposition of nonconforming:

11.3.2.1. Non-conforming product over PPM level will be replaced.

11.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

11.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1. There is any discrepancy in standard of quality assurance.

11.4.2. Additional requirement to be added in product specification.

11.4.3. Any other special problem.

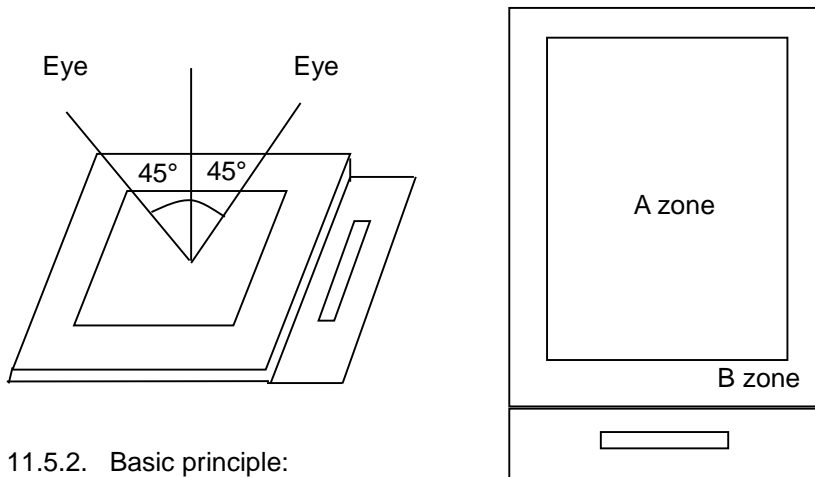
11.5.Standard of the Product Visual Inspection

11.5.1. Appearance inspection:

11.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,

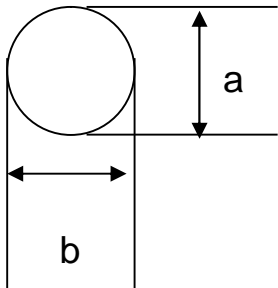


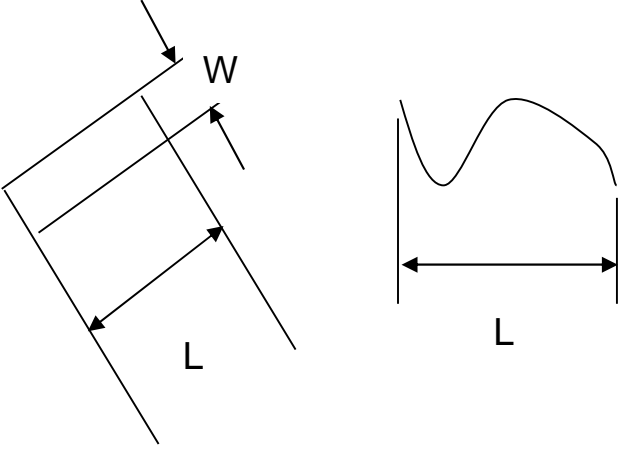
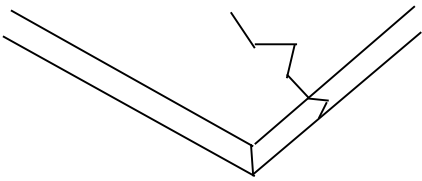
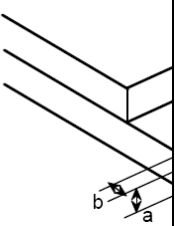
11.5.2. Basic principle:

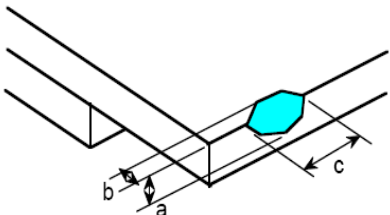
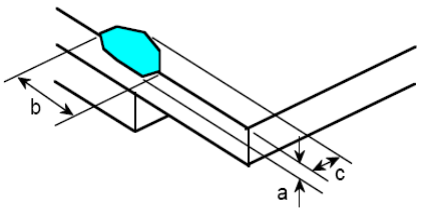
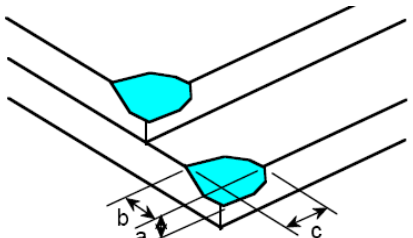
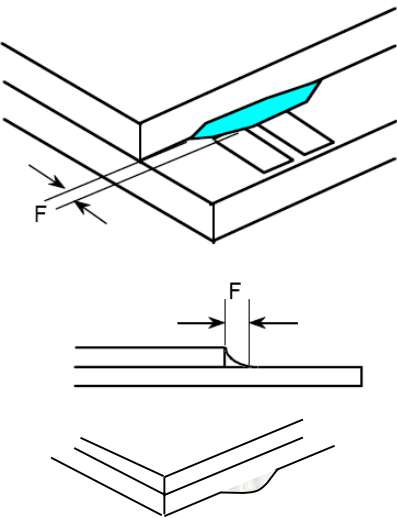
11.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

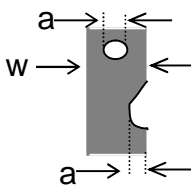
11.5.2.2. New item must be added on time when it is necessary.

11.6. Inspection Specification for the TFT module

No.	Item	Criteria (Unit: mm)																		
01	Black / White Spot Foreign Material (Round Type) Pinholes Stain Particles Inside Cell. (Minor Defect)	 <table border="1" data-bbox="916 1014 1418 1229"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.20$</td> <td></td> <td>Ignore</td> </tr> <tr> <td>$0.20 < \phi \leq 0.50$</td> <td></td> <td>$N \leq 3$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td></td> <td>0</td> </tr> </tbody> </table> <p>$\phi = (a + b) / 2$ Distance between 2 defects should more than 5mm apart.</p>	Size	Area	Acc. Qty	$\phi \leq 0.20$		Ignore	$0.20 < \phi \leq 0.50$		$N \leq 3$	$0.50 < \phi$		0						
Size	Area	Acc. Qty																		
$\phi \leq 0.20$		Ignore																		
$0.20 < \phi \leq 0.50$		$N \leq 3$																		
$0.50 < \phi$		0																		
02	Electrical Defect (Minor Defect)	<table border="1" data-bbox="544 1447 1409 1662"> <thead> <tr> <th>Bright Dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note1</th> </tr> </thead> <tbody> <tr> <td></td> <td>$N \leq 2$</td> <td>$N \leq 2$</td> </tr> <tr> <td>Dark Dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> </tr> <tr> <td>Total Dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> <td></td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> <td>Note 2</td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>	Bright Dot	Display Area	Total	Note1		$N \leq 2$	$N \leq 2$	Dark Dot	$N \leq 4$	$N \leq 4$	Total Dot	$N \leq 4$	$N \leq 4$		Mura	Not visible through 5% ND filters.		Note 2
Bright Dot	Display Area	Total	Note1																	
	$N \leq 2$	$N \leq 2$																		
Dark Dot	$N \leq 4$	$N \leq 4$																		
Total Dot	$N \leq 4$	$N \leq 4$																		
Mura	Not visible through 5% ND filters.		Note 2																	

<p>03</p>	<p>Black and White Line Scratch Foreign Material (Line Type) (Minor Defect)</p>	 <table border="1" data-bbox="603 683 1230 945"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td>$W \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.1 < W \leq 0.2$</td> <td>3</td> </tr> <tr> <td>$L > 2.5$</td> <td>$0.2 < W$</td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
<p>04</p>	<p>Glass Crack (Minor Defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>															
<p>05</p>	<p>Glass Chipping Pad Area: (Minor Defect)</p> 	<table border="1" data-bbox="746 1527 1219 1697"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>3</td> </tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$								
Length and Width	Acc. Qty																
$c > 3.0, b < 1.0$	1																
$c < 3.0, b < 1.0$	3																
$a < \text{Glass Thickness}$																	

<p>06</p>	<p>Glass Chipping Rear of Pad Area: (Minor Defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2">a < Glass Thickness</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	a < Glass Thickness	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
a < Glass Thickness												
<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor Defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2">a < Glass Thickness</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	a < Glass Thickness	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
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a < Glass Thickness												
<p>08</p>	<p>Glass Corner Chipping: (Minor Defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c < 3.0, b < 3.0$</td> <td>Ignore</td> </tr> <tr> <td colspan="2">a < Glass Thickness</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	a < Glass Thickness					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
a < Glass Thickness												
<p>09</p>	<p>Glass Burr: (Minor Defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$F < 1.0$</td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	<p>FPC Defect: (Minor Defect)</p> 	<p>10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>								
11	<p>Bubble on Polarizer (Minor Defect)</p>	<table border="1"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.30$</td> <td>Ignore</td> </tr> <tr> <td>$0.30 < \phi \leq 0.50$</td> <td>$N \leq 2$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td>$N = 0$</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\phi \leq 0.30$	Ignore	$0.30 < \phi \leq 0.50$	$N \leq 2$	$0.50 < \phi$	$N = 0$
Diameter	Acc. Qty									
$\phi \leq 0.30$	Ignore									
$0.30 < \phi \leq 0.50$	$N \leq 2$									
$0.50 < \phi$	$N = 0$									
12	<p>Dent on Polarizer (Minor defect)</p>	<table border="1"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.25$</td> <td>Ignore</td> </tr> <tr> <td>$0.25 < \phi \leq 0.50$</td> <td>$N \leq 4$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\phi \leq 0.25$	Ignore	$0.25 < \phi \leq 0.50$	$N \leq 4$	$0.50 < \phi$	None
Diameter	Acc. Qty									
$\phi \leq 0.25$	Ignore									
$0.25 < \phi \leq 0.50$	$N \leq 4$									
$0.50 < \phi$	None									
13	<p>Bezel</p>	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>								
14	<p>PCB</p>	<p>14.1 No distortion or contamination on PCB terminals. 14.2 All components on PCB must same as documented on the BOM/component layout. 14.3 Follow IPC-A-600F.</p>								
15	<p>Soldering</p>	<p>Follow IPC-A-610C standard</p>								
16	<p>Electrical Defect (Major Defect)</p>	<p>The below defects must be rejected. 16.1 Missing vertical / horizontal segment, 16.2 Abnormal Display. 16.3 No function or no display. 16.4 Current exceeds product specifications. 16.5 LCD viewing angle defect. 16.6 No Backlight. 16.7 Dark Backlight. 16.8 Touch Panel no function.</p>								

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

11.7. Classification of Defects

- 11.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.
- 11.7.2. Two minor defects are equal to one major in lot sampling inspection.

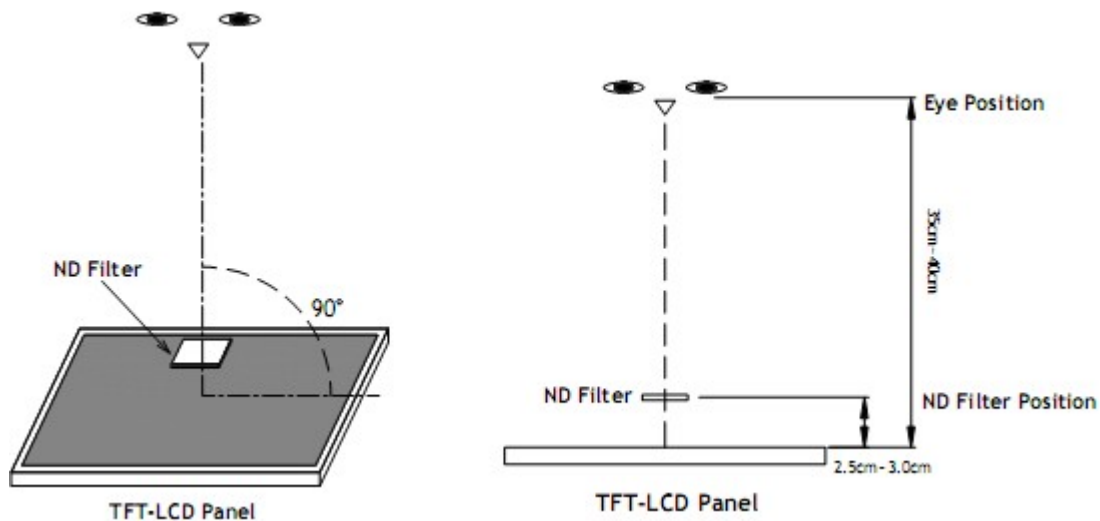
11.8. Identification/markings criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

11.9. Packing

- 11.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.
- 11.9.2. Modules inside package box should have compliant mark.
- 11.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm± 50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm± 50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

12. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	50°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	0°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity	50°C, 80%RH, 96Hrs	2	GB/T2423.3-2016
4	High Temperature Storage	60°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-20°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~60°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:1.5G X, Y, Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air: ± 15kV 150pF/330 Ω 5 times	2	GB/T17626.2-2018
		Contact: ± 8kV 150pF/330 Ω 5 times		
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

13. Precautions and Warranty

13.1.Safety

- 13.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 13.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

13.2.Handling

- 13.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 13.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

13.3.Storage

- 13.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 13.3.2. Strong light exposure causes degradation of polarizer and color filter.

13.4.Metal Pin (Apply to Products with Metal Pins)

13.4.1. Pins of LCD and Backlight

- 13.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

13.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

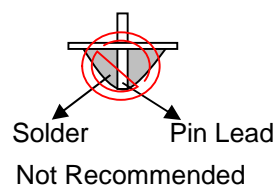
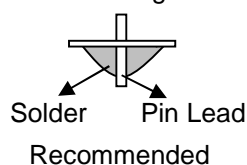
Maximum Solder Temperature: 370°C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20°C

Typical Soldering Time: ≤3s

13.4.1.3. Solder Wetting



13.4.2. Pins of EL

- 13.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.
- 13.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

13.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290°C

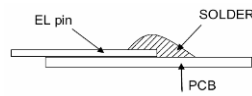
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

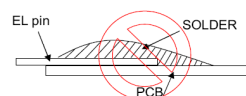
- 13.4.2.4. No horizontal press on the EL leads during soldering.

- 13.4.2.5. 180° bend EL leads three times is not allowed.

13.4.2.6. Solder Wetting

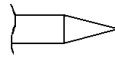


Recommended

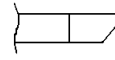


Not Recommended

13.4.2.7. The type of the solder iron:

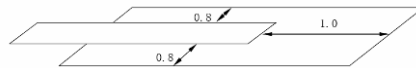


Recommended



Not Recommended

13.4.2.8. Solder Pad



13.5.Operation

- 13.5.1. Do not drive LCD with DC voltage
- 13.5.2. Response time will increase below lower temperature
- 13.5.3. Display may change color with different temperature
- 13.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.
- 13.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 13.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 13.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 13.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

13.6.Static Electricity

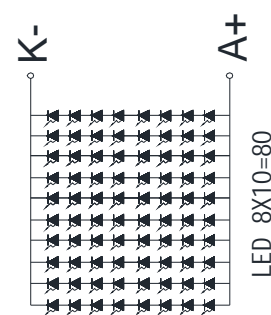
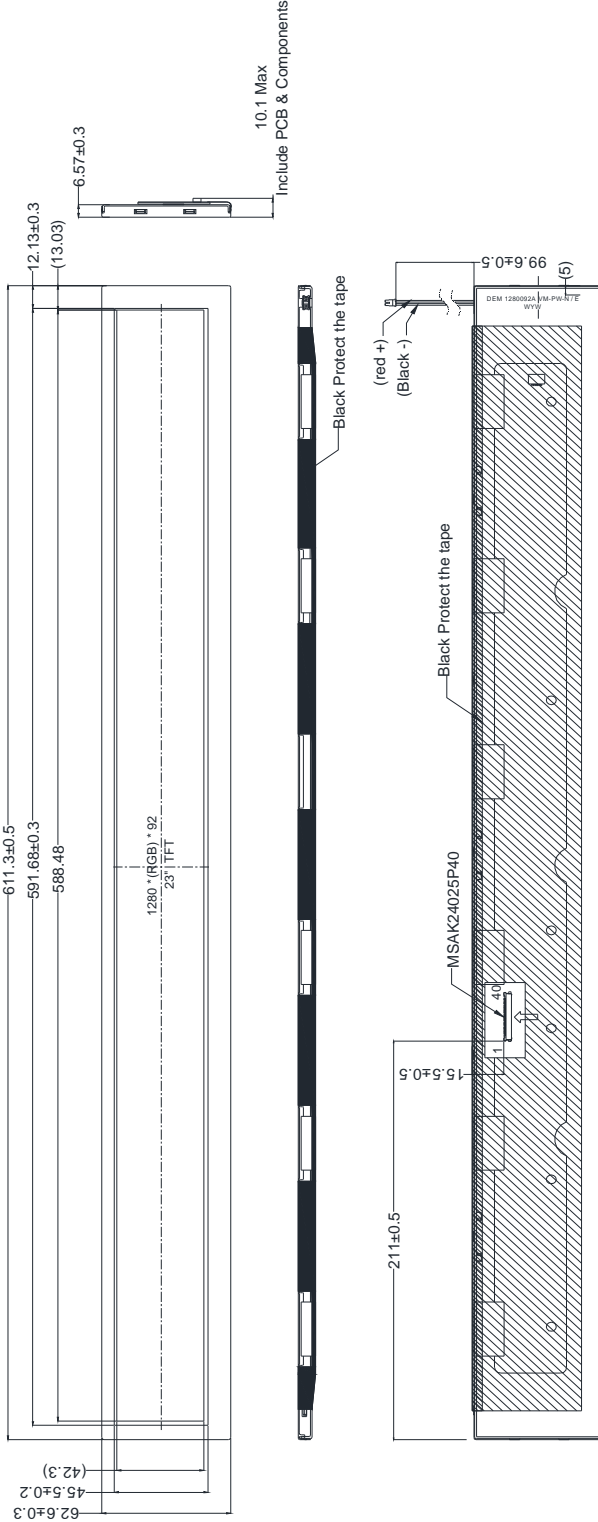
- 13.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 13.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 13.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

13.7.Limited Warranty

- 13.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 13.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 13.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

14. Outline Drawing

PIN ASSIGNMENT	
1.	Vcc
2.	Vcc
3.	Vcc
4.	GND_L
5.	GND_L
6.	GND_L
7.	Ag_Ing_Enable
8.	NC
9.	NC
10.	GND_L
11.	Vss0
12.	R1N0-
13.	R1N0+
14.	Vss1
15.	R1N1-
16.	R1N1+
17.	Vss2
18.	R1N2-
19.	R1N2+
20.	Vssk
21.	CLK-
22.	CLK+
23.	Vss3
24.	R1N3-
25.	R1N3+
26.	Vss4
27.	GND_L
28.	WP_EDID
29.	SCL_EDID
30.	SDA_EDID
31.	GND_L
32.	NC
33.	NC
34.	NC
35.	NC
36.	NC
37.	NC
38.	NC
39.	NC
40.	NC



- NOTES:
1. Display size: 23" TFT
 2. Viewing direction: Full Viewing
 3. Display mode: Transmissive/Normal Black
 4. Operation temperature: 0°C~+50°C
 5. Storage temperature: -20°C~+60°C
 6. Drive IC: Source(HX8159-K12-A)Gate(HX8658-H)
 7. Power supply voltage: 5V
 8. Backlight : White(80 LED)/22.8V(TYP)/400mA
Luminance: 1100cd/m2(TYP)
 9. ROHS must be complied
- * Unspecification tolerance are ± 0.4mm
 * The dimension with mark brackets "()" just for reference