

Display Elektronik GmbH

# DATA SHEET

LCD MODULE

## DEM 08201 SGH-LY

*Product Specification*

*Ver.: 5*

15.06.2020

# GENERAL SPECIFICATION

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MODULE NO. :

# DEM 08201 SGH-LY

CUSTOMER P/N:

Version No.	Change Description	Date
0	First Issue	26.10.2009
1	Change IC (AIP31066L-001)	16.08.2017
1.1.0	Original Version	24.03.2020
1.1.1	Change Production Line and Driver IC	26.03.2020
2	Change "BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATION" and update "PCB DRAWING"	13.04.2020
3	Update PCB drawing and description on Page 7	21.04.2020
4	Update PCB drawing and description on Page 7	27.04.2020
5	Change the pin hole to 1.0mm from 0.8mm in the drawing on page 5	15.06.2020

PREPARED BY: HP

DATE: 15.06.2020

APPROVED BY: MHI


DATE: 15.06.2020

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### 1. FUNCTIONS & FEATURES

I

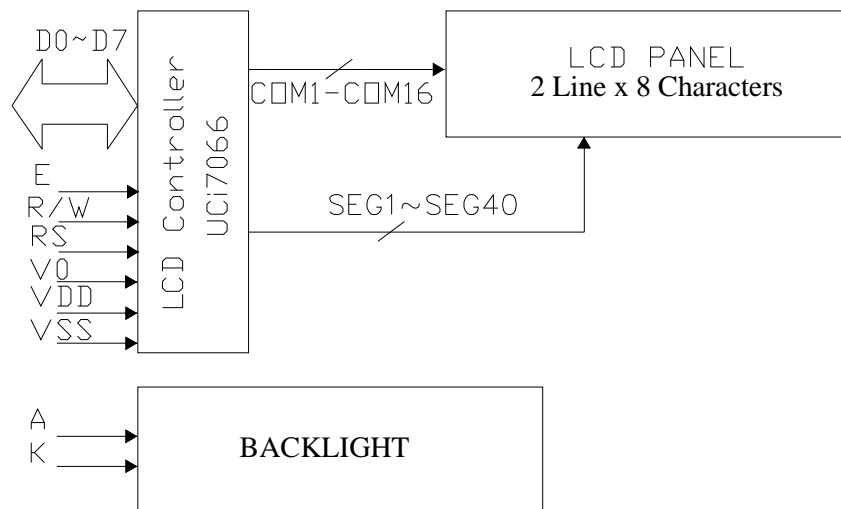
Module Model	LCD Type	Remark
DEM 08201 SGH-LY	STN Gray Transflective Positive Mode	---

- I Viewing Direction : 6 O'clock
- I Driving Scheme : 1/16 Duty Cycle, 1/5 Bias
- I Power Supply Voltage : 5.0V
- I V<sub>LCD</sub> Adjustable For Best Contrast(V<sub>DD</sub>-V<sub>0</sub>) : 4.0V
- I Backlight Color : Yellow-Green
- I Display Contents : 8 x 2 Characters (5 x 8 dots)
- I Internal Memory : CGROM (13,200 bits )  
: CGRAM (64 byte )  
: DDRAM (80 \* 8 bits for Digits)
- I RoHS Compliant

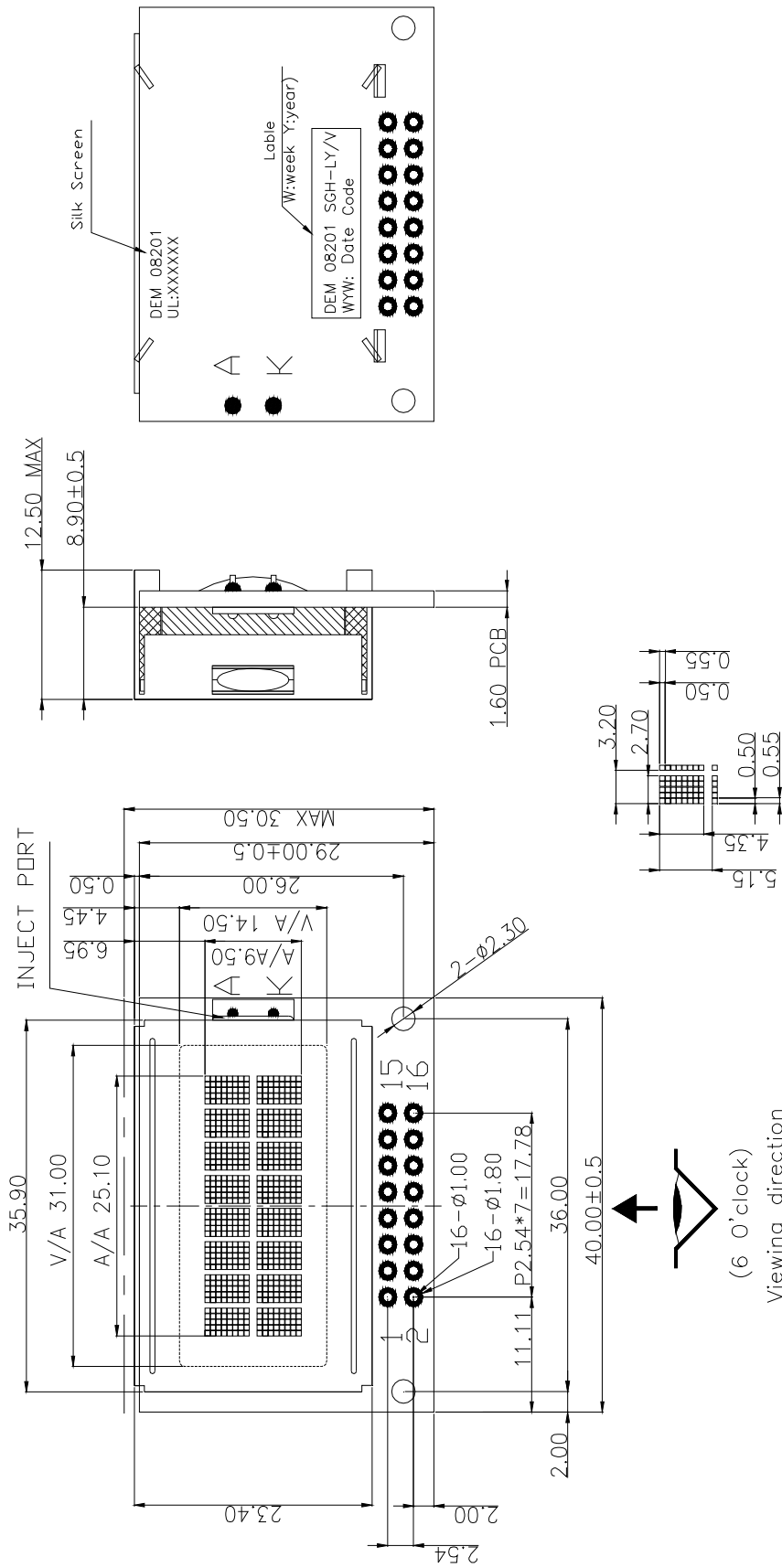
### 2. MECHANICAL SPECIFICATIONS

- I Module Size : 40.00 x 29.00(30.50) x 12.50(max)mm
- I Character Pitch : 3.20 x 5.15 mm
- I Character Size : 2.70 x 4.35 mm
- I Character Font : 5 x 8 dots
- I Dot Size : 0.50 x 0.50 mm
- I Dot Pitch : 0.55 x 0.55 mm

### 3. BLOCK DIAGRAM



4. EXTERNAL DIMENSIONS (  Unit: mm)

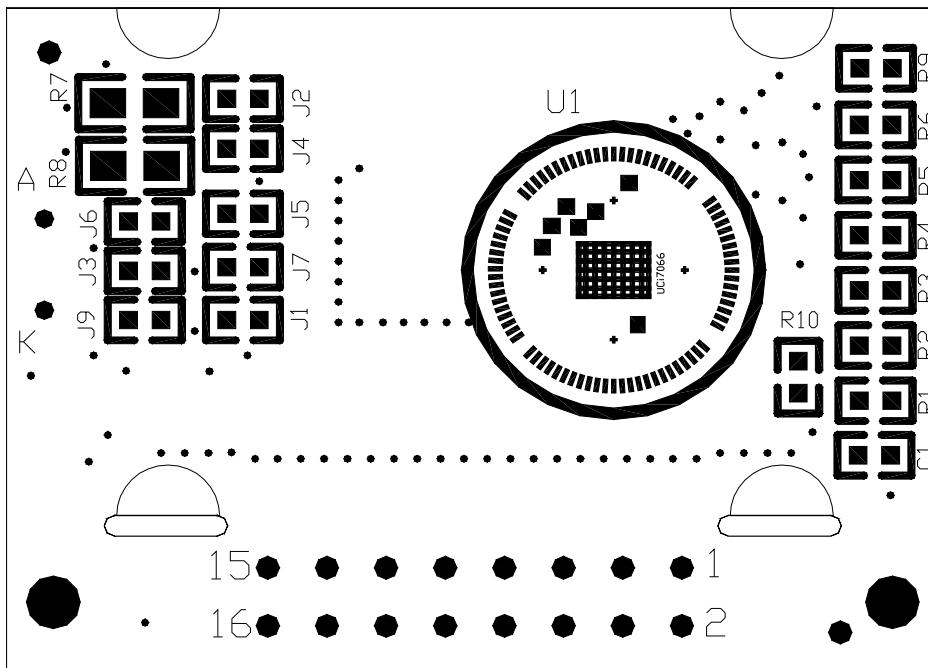


Remarks:  
 1. Unmarked tolerance is ±0.3  
 2. All materials comply with RoHs  
 3. : critical dimension.

## 5. PIN ASSIGNMENT

Pin No.	Symbol	Function	
1	R/W	Read or Write selection. L: Write H: Read	
		<b>RS</b> <b>R/W</b> <b>Operation</b>	
		L        L        Instruction Write (MPU writes instruction code into IR)	
		L        H        DB7: Read Busy Flag, DB6~DB0: Address Counter	
		H        L        Data Write (MPU writes data into DR)	
H        H        Data Read (MPU reads data from DR)			
2	E	Starts Data-Read or Data-Write	
3	RS	Register selection. L: (for write) Instruction register Busy flag (for read) Address Counter H: (for write and read) Data register	
4	V0	Power supply for LCD driver	
5	VDD	Power supply for Logic(+5V)	
6	VSS	Power GND(0V)	
7	LEDA	Anode of Backlight	
8	LEDK	Cathode of Backlight	
9	DB0	Bi-directional tri-state data bus pins. Used for data transfer and receive between the MPU and the UCi7066c. DB7 can be used as a busy flag. DB3~DB0 are not used during 4-bit operation.	
10	DB7		
11	DB1		
12	DB6		
13	DB2		<b>MODE</b> <b>SERMODE</b> <b>DB7</b> <b>DB6</b> <b>DB5</b> <b>DB4</b> <b>DB3</b> <b>DB2</b> <b>DB1</b> <b>DB0</b>
			6800 - 4bit    L                    D3    D2    D1    D0    -    -    -    -
14	DB5		6800 - 8bit    L                    D7    D6    D5    D4    D3    D2    D1    D0
15	DB3		In original design, only one chip select I/O, specified in DB[2].
16	DB4		

## 6. PCB DRAWING AND DESCRIPTION



### 6.1 DESCRIPTION:

6-1-1.The polarity of the pin 7 and the pin 8:

LED Polarity(1)	
7 Pin	8 Pin
Anode	Cathode
J3=J5=open J2=J4=closed	

LED Polarity(2)	
7 Pin	8 Pin
Cathode	Anode
J3=J5= closed J2=J4= open	

※Note: In application module, J3=J5= open and J2=J4=Closed

6-1-2. The metal-bezel is set be on ground when the J1 is closed

※Note: In application module, J1= closed

6-1-3. The LED resistor on board are used when J6 is open.

※Note: In application module, J6=open

6-1-4.The R7 and the R8 are the LED resistor.

※Note: In application module, R7 =(15) Ohm, R8=open

6-1-5. The mounting holes are set on ground when J7 is closed..

※Note: In application module, J7=closed

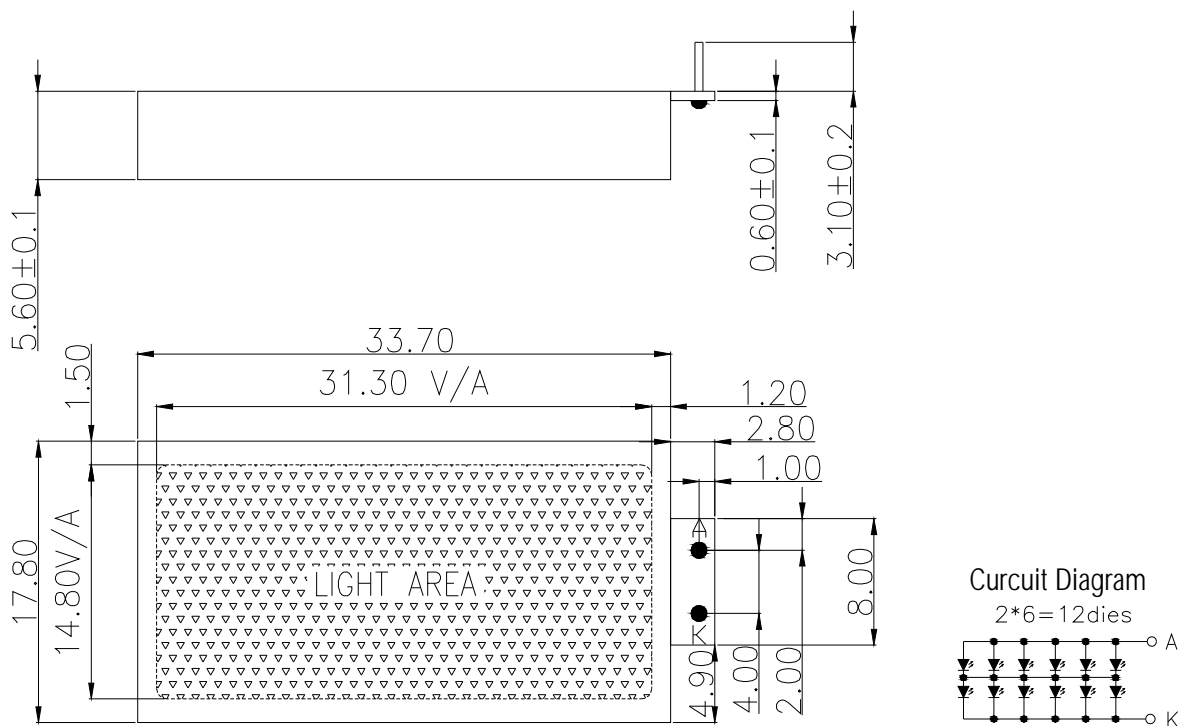
6-1-6. The LEDK is set be on ground when the J9 is closed

※Note: In application module, J9= open

### 7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATION

#### Electrical-Optical Characteristics (Ta=25° C)

Item	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Forward Voltage	Vf	3.8	4.0	4.3	V	If= 60 mA
Dominant Wave Length	$\lambda_D$	569	572	575	nm	
Uniformity	Avg	65	75		%	
Luminance	Lv	160	240		cd/m <sup>2</sup>	



REMARKS:  
1,UNMARKED TOLERANCE IS ±0.3,  
2,COLOR:Bottom Yellow-green,  
3,THE MATERIAL COMPLY WITH ROHS.

### 8. MAXIMUM ABSOLUTE POWER RATINGS (Ta=-25°C)

Item	Symbol	Standard value	Unit
Power Supply Voltage (1)	V <sub>DD</sub>	-0.3~+7.0	V
Power Supply Voltage (2)	V <sub>LCD</sub>	V <sub>DD</sub> -15.0~V <sub>DD</sub> +0.3	V
Input Voltage	V <sub>IN</sub>	-0.3~V <sub>DD</sub> +0.3	V
Operating Temperature	T <sub>opr</sub>	-20~+70	°C
Storage Temperature	T <sub>stg</sub>	-30~+80	°C



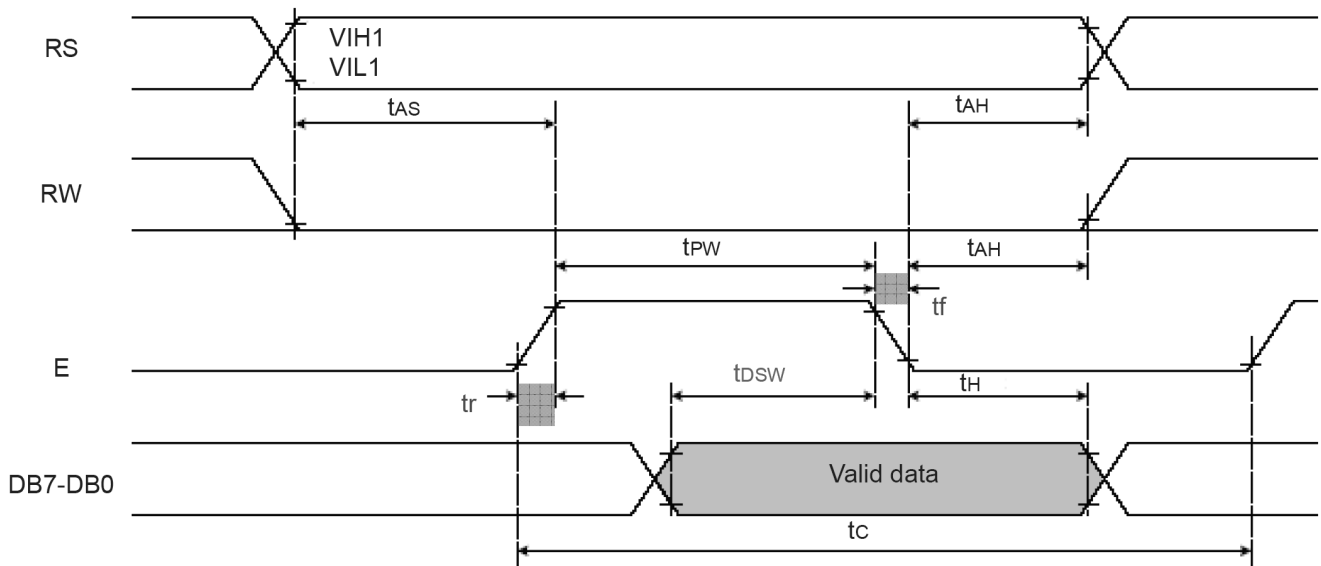
## 9. ELECTRICAL CHARACTERISTICS

### 9-1 DC Characteristics(VDD=4.5V~5.5V)

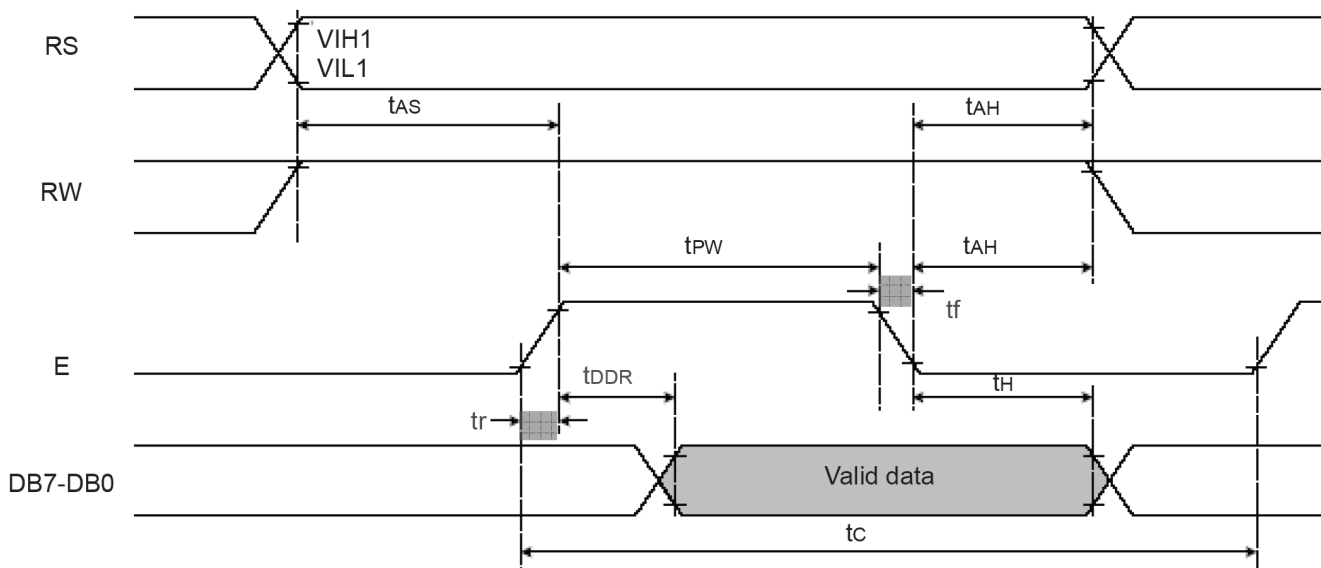
Item	Symbol	Standard Value			Test Condition	Unit
		MIN	TYP	MAX		
Operating Voltage	VDD	4.7	5	5.3	-----	V
Supply Current	IDD	----	TBD	----	VDD=5V,fosc=270kHz	mA
LCD Driving Voltage	VLCD(VDD-V0)	4.4	4.6	4.9	Ta = -20°C	V
		3.8	4.0	4.3	Ta = 25°C	
		3.4	3.6	3.8	Ta = 70°C	

### 9-2 AC Characteristics (VDD=4.5V~5.5V)

#### 9-2-1 6800 mode



(6800 Write data to UCi7066c)



(6800 Read data from UCi7066c)

TA = 25°C, VCC=4.5V~5V

Symbol	Characteristic	Test Condition	Min.	Typ.	Max.	Unit
<b>Internal Clock Operation</b>						
fosc	OSC Frequency	R=91KΩ	190	270	350	KHz
<b>External Clock Operation</b>						
fEX	External Frequency	--	125	270	410	KHz
	Duty Cycle	--	45	50	55	%
tR, tF	Rising/Falling Time	--	--	--	0.2	uS
<b>Write Mode (MPU writes data to UCi7066)</b>						
tc	Enable Cycle Time	Pin E	1200	--	--	nS
tpw	Enable Pulse Width	Pin E	140	--	--	nS
tR, tF	Rising/Falling Time	Pin E	--	--	25	nS
tAS	Address Setup Time	Pin: RS, RW, E	0	--	--	nS
tAH	Address Hold Time	Pin: RS, RW, E	10	--	--	nS
tDSW	Data Setup Time	Pin: DB7~DB0	40	--	--	nS
tH	Data Hold Time	Pin: DB7~DB0	10	--	--	nS
<b>Read Mode (MPU reads data from UCi7066)</b>						
tc	Enable Cycle Time	Pin E	1200	--	--	nS
tpw	Enable Pulse Width	Pin E	140	--	--	nS
tR, tF	Rising/Falling Time	Pin E	--	--	25	nS
tAS	Address Setup Time	Pin: RS, RW, E	0	--	--	nS
tAH	Address Hold Time	Pin: RS, RW, E	10	--	--	nS
tDDR	Data Setup Time	Pin: DB7~DB0	--	--	100	nS
tH	Data Hold Time	Pin: DB7~DB0	10	--	--	nS

## 10. CONTROL AND DISPLAY COMMAND

The following is a list of host commands supported by UCi7066

**R/S**: 0: Control, 1: Data

**W/R**: 0: Write Cycle, 1: Read Cycle

**D7-D0**: -: Don't Care

#	Command	RS	R/W	D7	D6	D5	D4	D3	D2	D1	D0	Action
1	Clear Display	0	0	0	0	0	0	0	0	0	1	Clear the screen
2	Return Home	0	0	0	0	0	0	0	0	1	-	Move cursor to HOME
3	Set Entry Mode	0	0	0	0	0	0	0	1	I/D	S	I/D: Left / Right S: Shift OFF/ON
4	Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D: Display OFF / ON C: Cursor OFF / ON B: Blink OFF / ON
5	Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	S/C: Screen / Cursor R/L Right / Left
6	Set Function	0	0	0	0	1	DL	N	F	-	-	DL: 4-bit / 8-bit, N: 1-line / 2-line F: 5x8 / 5x11
7	Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	
8	Set DDRAM address	0	0	1	AC12	AC11	AC10	AC9	AC8	AC7	AC6	
9	Read Busy Flag and address	0	1	BF	AC19	AC18	AC17	AC16	AC15	AC14	AC13	
10	Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data to RAM
11	Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from RAM

**Note:**

Ensure that UCi7066 is not in the BUSY state (BF = 0) before sending an instruction from the MPU to the UCi7066. If an instruction

is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

### 11. STANDARD CHARACTER PATTERN (UCi7066c-01)

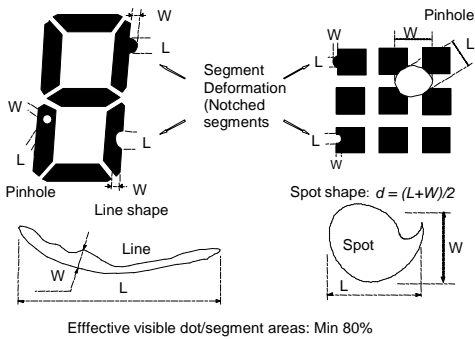
Upper(4bit) Lower(4bit)		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	10010	1011	1100	1101	1110	1111
0000	CG RAM (1)			0	a	P	r	r	r				一	3	E	o	P
0001	(2)		!	1	A	a	a					7	7	4	a	a	
0010	(3)		"	2	B	R	b	r				7	4	W	X	P	P
0011	(4)		#	3	C	S	c	s				J	9	T	E	e	o
0100	(5)		*	4	D	T	d	t				V	1	1	h	h	o
0101	(6)		%	5	E	U	e	u				*	*	*	1	e	o
0110	(7)		&	6	F	U	f	u				3	h	二	3	P	Z
0111	(8)		'	7	G	U	g	u				7	*	Z	7	g	u
1000	(1)		<	8	H	X	h	x				4	9	*	U	J	Z
1001	(2)		>	9	I	Y	i	y				5	7	J	U	U	U
1010	(3)		*	:	J	Z	j	z				二	3	U	J	J	7
1011	(4)		+	;	K	C	k	c				*	7	E	U	K	A
1100	(5)		.	<	L	*	l	l				7	3	7	7	o	u
1101	(6)		—	—	M	I	m	i				3	Z	X	U	k	+
1110	(7)		.	>	N	^	n	^				3	E	7	7	7	
1111	(8)		/	?	O	_	o	*				W	U	7	7	o	

## 12. QUALITY DESCRIPTION

### DEFECT SPECIFICATION:

Specific type-related items are covered in this sheet.

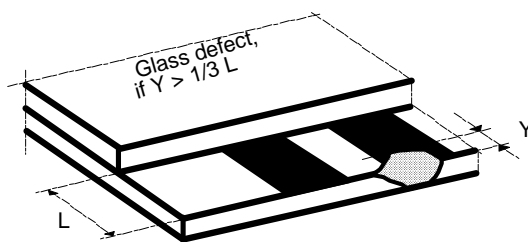
- a: Table for Cosmetic defects  
 (Note: nc = not counted).  
 Sizes and number of defects  
 (Max. Qty)



#### Examples/Shapes

- b: Glass defects  
 b1: Glass defects at contact ledge

b2: Glass chipping in other areas shall not be in conflict



with the product's function.

Defect Type	Max. defect size [ $\mu\text{m}$ ] d or L W	Max. Quantity.
Black or White Spots	$d \leq 100$	nc
	$100 < d \leq 200$	5
Black or White Lines	-- $W \leq 10$	nc
	$L \leq 5000$ $W \leq 30$	3
	$L \leq 2000$ $W \leq 50$	2
Pinhole	$d \leq 100$	nc
	$100 < d \leq 200$	1/segment
(Total defects)		(5)
Segment Deformation	$W \leq 100$	nc
Bubble (e.g. under pola)	$d \leq 150$	nc
	$200 < d \leq 400$	3
	$400 < d \leq 600$	1

### 13. MODULE ACCEPT QUALITY LEVEL (AQL)

Inspection Plan: ANSI Z-1.4, Normal Inspection Level II, Single Sampling Plan

### 14. RELIABILITY TEST

Operating life time: Longer than 50000 hours

(at room temperature without direct irradiation of sunlight)

Reliability characteristics shall meet following requirements.

TEMPERATURE TESTS	NORMAL GRADE
High Temperature Storage	+80°C x 96hrs
Low Temperature Storage	-30°Cx 96hrs
High Temperature Operation	+70°C x 96hrs
Low Temperature Operation	-20°C x 96hrs
High Temperature, High Humidity	+60°C ,95%RH x 96hrs
Thermal Shock	<div style="display: flex; align-items: center; justify-content: center;"> <div style="text-align: center;"> <p>-20°C x30min.</p> <p>10s</p> </div> <div style="margin: 0 10px;"> <p>↓</p> </div> <div style="text-align: center;"> <p>5Cycles</p> <p>+70°C x 30min.</p> </div> </div>
Vibration Test	Frequency x Swing x Time 40Hz x 4mm x 4hrs
Drop Test	Drop height x Times 1.0m x 6times

## 15. LCD MODULES HANDLING PRECAUTIONS

- n Please remove the protection foil of polarizer before using.
- n The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
  
- n If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
  
- n Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
  
- n The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
  
- n To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling the LCD module.
  - Tools required for assembly, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
  
- n Storage precautions  
When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C).Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

## 16. OTHERS

- n Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
  
- n If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
  
- n To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
  - Exposed area of the printed circuit board
  - Terminal electrode sections